

RCW Sections Affected by 2020 Statutes

RCW	Action	Bill/Ch.	Sec.	RCW	Action	Bill/Ch.	Sec.
<u>1</u>	ADD	<u>57</u>	1-20	<u>9.35.005</u>	AMD	<u>312</u>	706
<u>1.16.050</u>	REMD	<u>74</u>	2	<u>9.38.060</u>	AMD	<u>57</u>	24
<u>2.08.062</u>	AMD	<u>53</u>	1	<u>9.41</u>	ADD	<u>28</u>	4
<u>2.08.065</u>	AMD	<u>53</u>	3	<u>9.41</u>	ADD	<u>36</u>	1
<u>2.28</u>	ADD	<u>37</u>	2-6	<u>9.41</u>	ADD	<u>189</u>	1
<u>2.56</u>	ADD	<u>296</u>	6	<u>9.41.010</u>	REMD	<u>29</u>	3
<u>2.56.150</u>	AMD	<u>312</u>	701	<u>9.41.040</u>	REMD	<u>29</u>	4
<u>2.70.060</u>	AMD	<u>33</u>	3	<u>9.41.042</u>	AMD	<u>18</u>	6
<u>2.70.070</u>	AMD	<u>33</u>	4	<u>9.41.047</u>	REMD	<u>302</u>	60
<u>2.70.080</u>	AMD	<u>33</u>	5	<u>9.41.049</u>	AMD	<u>302</u>	61
<u>2.70.090</u>	AMD	<u>33</u>	6	<u>9.41.070</u>	REMD	<u>148</u>	2
<u>2.72.005</u>	AMD	<u>312</u>	401	<u>9.41.114</u>	AMD	<u>28</u>	5
<u>2.72.010</u>	REMD	<u>312</u>	402	<u>9.41.340</u>	AMD	<u>29</u>	5
<u>2.72.020</u>	AMD	<u>312</u>	403	<u>9.41.345</u>	AMD	<u>29</u>	6
<u>2.72.030</u>	AMD	<u>312</u>	404	<u>9.41.801</u>	AMD	<u>126</u>	1
<u>3.02</u>	ADD	<u>37</u>	7	<u>9.46</u>	ADD	<u>127</u>	2,5,9
<u>3.72.005</u>	AMD	<u>191</u>	1				11
<u>3.72.010</u>	AMD	<u>191</u>	2	<u>9.46.0209</u>	AMD	<u>150</u>	1
<u>3.72.020</u>	AMD	<u>191</u>	3	<u>9.46.070</u>	AMD	<u>127</u>	3
<u>3.72.040</u>	AMD	<u>191</u>	4	<u>9.46.090</u>	AMD	<u>127</u>	12
<u>4.16.190</u>	AMD	<u>312</u>	702	<u>9.46.110</u>	AMD	<u>70</u>	1
<u>4.24</u>	ADD	<u>149</u>	1	<u>9.46.130</u>	AMD	<u>127</u>	4
<u>4.24</u>	ADD	<u>344</u>	3	<u>9.46.190</u>	AMD	<u>127</u>	6
<u>4.92.100</u>	AMD	<u>57</u>	21	<u>9.46.210</u>	AMD	<u>127</u>	7
<u>5.50.010</u>	AMD	<u>57</u>	22	<u>9.46.220</u>	AMD	<u>127</u>	8
<u>5.50.030</u>	AMD	<u>57</u>	23	<u>9.46.240</u>	AMD	<u>127</u>	10
<u>5.60.060</u>	AMD	<u>42</u>	1	<u>9.94A</u>	ADD	<u>55</u>	1
<u>5.60.060</u>	AMD	<u>302</u>	113	<u>9.94A</u>	ADD	<u>275</u>	2
<u>5.70</u>	ADD	<u>26</u>	2,3,18	<u>9.94A.030</u>	REMD	<u>137</u>	1
<u>5.70.010</u>	AMD	<u>26</u>	1	<u>9.94A.030</u>	REMD	<u>252</u>	4
<u>7.28.090</u>	AMD	<u>312</u>	703	<u>9.94A.030</u>	REMD	<u>296</u>	2
<u>7.36.020</u>	AMD	<u>312</u>	704	<u>9.94A.501</u>	AMD	<u>275</u>	1
<u>7.68</u>	ADD	<u>331</u>	2,3,11	<u>9.94A.515</u>	REMD	<u>344</u>	4
<u>7.68.020</u>	AMD	<u>274</u>	1	<u>9.94A.533</u>	AMD	<u>141</u>	1
<u>7.68.030</u>	AMD	<u>80</u>	12	<u>9.94A.533</u>	AMD	<u>330</u>	1
<u>7.68.060</u>	AMD	<u>308</u>	1	<u>9.94A.589</u>	AMD	<u>276</u>	1
<u>7.68.061</u>	AMD	<u>308</u>	2	<u>9.94A.631</u>	AMD	<u>82</u>	2
<u>7.68.070</u>	AMD	<u>308</u>	3	<u>9.94A.655</u>	AMD	<u>137</u>	2
<u>7.68.801</u>	AMD	<u>331</u>	7	<u>9.94A.6551</u>	AMD	<u>137</u>	3
<u>7.70.065</u>	REMD	<u>312</u>	705	<u>9.94A.660</u>	REMD	<u>252</u>	1
<u>7.77.060</u>	AMD	<u>29</u>	1	<u>9.94A.662</u>	AMD	<u>252</u>	2
<u>7.77.080</u>	AMD	<u>29</u>	2	<u>9.94A.664</u>	REMD	<u>252</u>	3
<u>7.84.070</u>	AMD	<u>38</u>	5	<u>9.94A.716</u>	AMD	<u>82</u>	3
<u>7.84.100</u>	AMD	<u>268</u>	1	<u>9.94A.729</u>	AMD	<u>330</u>	2
<u>7.90.010</u>	AMD	<u>296</u>	3	<u>9.94A.737</u>	AMD	<u>82</u>	1
<u>7.92.020</u>	AMD	<u>296</u>	4	<u>9.94B.050</u>	AMD	<u>276</u>	2
<u>7.94.090</u>	AMD	<u>126</u>	2	<u>9.96.060</u>	REMD	<u>29</u>	18

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9A.08	ADD	3	1	11.88.110	REP	312	904
9A.16	ADD	3	2	11.88.115	REP	312	904
9A.36.011	AMD	76	16	11.88.120	REP	312	904
9A.36.041	AMD	29	7	11.88.125	REP	312	904
9A.42.010	AMD	18	7	11.88.127	REP	312	904
9A.44.010	AMD	312	707	11.88.130	REP	312	904
9A.44.138	AMD	167	5	11.88.140	REP	312	904
9A.44.140	AMD	249	2	11.88.150	REP	312	904
9A.60	ADD	34	2	11.88.160	REP	312	904
9A.76	ADD	19	2	11.88.170	REP	312	904
9A.76.170	AMD	19	1	11.88.900	REP	312	904
9A.83.030	AMD	62	1	11.90.020	AMD	312	501
9A.84.040	AMD	344	2	11.90.210	AMD	312	712
9A.88.030	AMD	331	4	11.90.230	AMD	312	502
10.14.055	AMD	29	8	11.90.250	AMD	312	503
10.21.055	AMD	330	3	11.90.400	AMD	312	504
10.22.010	AMD	29	9	11.90.410	AMD	312	505
10.31.100	REMD	29	10	11.92.010	REP	312	904
10.66.010	AMD	29	11	11.92.035	REP	312	904
10.77.088	REMD	18	4	11.92.040	REP	312	904
10.77.210	AMD	81	1	11.92.043	REP	312	904
10.79.080	AMD	57	25	11.92.050	REP	312	904
10.95.020	AMD	29	12	11.92.053	REP	312	904
10.97.050	AMD	184	2	11.92.056	REP	312	904
10.99.020	REMD	296	5	11.92.060	REP	312	904
11	ADD	303	1-18	11.92.090	REP	312	904
			20	11.92.096	REP	312	904
11.02.005	AMD	312	708	11.92.100	REP	312	904
11.28.120	AMD	312	405	11.92.110	REP	312	904
11.28.185	AMD	312	709	11.92.115	REP	312	904
11.76.080	AMD	312	710	11.92.120	REP	312	904
11.86.021	AMD	312	711	11.92.125	REP	312	904
11.88.005	REP	312	904	11.92.130	REP	312	904
11.88.008	REP	312	904	11.92.140	REP	312	904
11.88.010	REP	312	904	11.92.150	REP	312	904
11.88.020	REP	312	904	11.92.160	REP	312	904
11.88.030	REP	312	904	11.92.170	REP	312	904
11.88.040	REP	312	904	11.92.180	REP	312	904
11.88.045	REP	312	904	11.92.185	REP	312	904
11.88.080	REP	312	904	11.92.190	REP	312	904
11.88.090	REP	312	904	11.92.195	REP	312	904
11.88.093	REP	312	904	11.96A.050	AMD	312	713
11.88.095	REP	312	904	11.96A.080	AMD	312	714
11.88.097	REP	312	904	11.96A.120	AMD	312	715
11.88.100	REP	312	904	11.96A.130	AMD	312	716
11.88.105	REP	312	904	11.96A.150	AMD	312	717
11.88.107	REP	312	904	11.96A.220	AMD	312	718

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<u>11.98A.010</u>	REP	<u>303</u>	19	<u>11.130.280</u>	AMD	<u>312</u>	309
<u>11.98A.020</u>	REP	<u>303</u>	19	<u>11.130.285</u>	AMD	<u>312</u>	202
<u>11.98A.030</u>	REP	<u>303</u>	19	<u>11.130.290</u>	AMD	<u>312</u>	203
<u>11.98A.040</u>	REP	<u>303</u>	19	<u>11.130.295</u>	AMD	<u>312</u>	314
<u>11.98A.050</u>	REP	<u>303</u>	19	<u>11.130.320</u>	AMD	<u>312</u>	204
<u>11.98A.060</u>	REP	<u>303</u>	19	<u>11.130.330</u>	AMD	<u>312</u>	205
<u>11.98A.070</u>	REP	<u>303</u>	19	<u>11.130.335</u>	AMD	<u>312</u>	206
<u>11.98A.080</u>	REP	<u>303</u>	19	<u>11.130.340</u>	AMD	<u>312</u>	207
<u>11.98A.090</u>	REP	<u>303</u>	19	<u>11.130.345</u>	AMD	<u>312</u>	208
<u>11.98A.100</u>	REP	<u>303</u>	19	<u>11.130.360</u>	AMD	<u>312</u>	209
<u>11.98A.110</u>	REP	<u>303</u>	19	<u>11.130.370</u>	AMD	<u>312</u>	210
<u>11.98A.120</u>	REP	<u>303</u>	19	<u>11.130.380</u>	AMD	<u>312</u>	310
<u>11.98A.900</u>	REP	<u>303</u>	19	<u>11.130.385</u>	AMD	<u>312</u>	211
<u>11.103.030</u>	AMD	<u>312</u>	719	<u>11.130.390</u>	AMD	<u>312</u>	212
<u>11.107.060</u>	AMD	<u>312</u>	720	<u>11.130.410</u>	AMD	<u>312</u>	213
<u>11.120.140</u>	AMD	<u>312</u>	721	<u>11.130.415</u>	AMD	<u>312</u>	214
<u>11.125.080</u>	AMD	<u>312</u>	320	<u>11.130.420</u>	AMD	<u>312</u>	215
<u>11.125.400</u>	AMD	<u>312</u>	722	<u>11.130.425</u>	AMD	<u>312</u>	216
<u>11.125.410</u>	AMD	<u>312</u>	723	<u>11.130.430</u>	AMD	<u>312</u>	217
<u>11.130</u>	ADD	<u>312</u>	112,122	<u>11.130.435</u>	AMD	<u>312</u>	218
			224,226	<u>11.130.505</u>	AMD	<u>312</u>	219
			321	<u>11.130.515</u>	AMD	<u>312</u>	220
			601-612	<u>11.130.520</u>	AMD	<u>312</u>	221
			801,906	<u>11.130.530</u>	AMD	<u>312</u>	222
<u>11.130.010</u>	AMD	<u>312</u>	301	<u>11.130.550</u>	AMD	<u>312</u>	223
<u>11.130.035</u>	AMD	<u>312</u>	302	<u>11.130.585</u>	AMD	<u>312</u>	315
<u>11.130.040</u>	AMD	<u>312</u>	303	<u>11.130.600</u>	AMD	<u>312</u>	316
<u>11.130.080</u>	AMD	<u>312</u>	312	<u>11.130.605</u>	AMD	<u>312</u>	311
<u>11.130.100</u>	AMD	<u>312</u>	304	<u>11.130.610</u>	AMD	<u>312</u>	318
<u>11.130.105</u>	AMD	<u>312</u>	305	<u>11.130.615</u>	AMD	<u>312</u>	319
<u>11.130.115</u>	AMD	<u>312</u>	306	<u>11.130.625</u>	AMD	<u>312</u>	317
<u>11.130.120</u>	AMD	<u>312</u>	313	<u>11.130.670</u>	AMD	<u>312</u>	225
<u>11.130.140</u>	AMD	<u>312</u>	307	<u>11.130.915</u>	AMD	<u>312</u>	902
<u>11.130.185</u>	AMD	<u>312</u>	101	<u>12.40.105</u>	AMD	<u>25</u>	1
<u>11.130.190</u>	AMD	<u>312</u>	102	<u>13</u>	ADD	<u>333</u>	1-7
<u>11.130.195</u>	AMD	<u>312</u>	103				9
<u>11.130.205</u>	AMD	<u>312</u>	104	<u>13.04.030</u>	REMD	<u>41</u>	4
<u>11.130.210</u>	AMD	<u>312</u>	105	<u>13.04.116</u>	AMD	<u>333</u>	8
<u>11.130.215</u>	AMD	<u>312</u>	106	<u>13.04.155</u>	AMD	<u>167</u>	6
<u>11.130.220</u>	AMD	<u>312</u>	107	<u>13.06.050</u>	AMD	<u>262</u>	2
<u>11.130.225</u>	AMD	<u>312</u>	108	<u>13.32A</u>	ADD	<u>51</u>	4
<u>11.130.230</u>	AMD	<u>312</u>	109	<u>13.32A.030</u>	AMD	<u>51</u>	1
<u>11.130.240</u>	AMD	<u>312</u>	110	<u>13.32A.040</u>	AMD	<u>51</u>	2
<u>11.130.245</u>	AMD	<u>312</u>	111	<u>13.32A.150</u>	AMD	<u>51</u>	3
<u>11.130.250</u>	AMD	<u>312</u>	113	<u>13.32A.160</u>	AMD	<u>312</u>	724
<u>11.130.265</u>	AMD	<u>312</u>	308	<u>13.34.030</u>	REMD	<u>312</u>	114
<u>11.130.275</u>	AMD	<u>312</u>	201	<u>13.34.062</u>	AMD	<u>312</u>	115

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<u>13.34.110</u>	AMD	<u>312</u>	116	<u>18.20</u>	ADD	<u>270</u>	5
<u>13.34.136</u>	AMD	<u>312</u>	117	<u>18.20.020</u>	REMD	<u>312</u>	726
<u>13.34.145</u>	AMD	<u>312</u>	118	<u>18.22.013</u>	AMD	<u>248</u>	1
<u>13.34.155</u>	AMD	<u>312</u>	119	<u>18.22.014</u>	AMD	<u>248</u>	2
<u>13.34.210</u>	AMD	<u>312</u>	120	<u>18.27.060</u>	AMD	<u>153</u>	21
<u>13.34.270</u>	REP	<u>41</u>	5	<u>18.27.090</u>	AMD	<u>153</u>	22
<u>13.34.270</u>	AMD	<u>312</u>	725	<u>18.27.114</u>	AMD	<u>57</u>	26
<u>13.40.0357</u>	AMD	<u>18</u>	8	<u>18.35.040</u>	AMD	<u>76</u>	17
<u>13.40.070</u>	AMD	<u>331</u>	5	<u>18.43.035</u>	AMD	<u>47</u>	1
<u>13.40.160</u>	AMD	<u>18</u>	9	<u>18.43.200</u>	AMD	<u>47</u>	2
<u>13.40.162</u>	AMD	<u>249</u>	1	<u>18.51</u>	ADD	<u>270</u>	6
<u>13.40.193</u>	AMD	<u>18</u>	10	<u>18.51.091</u>	AMD	<u>263</u>	1
<u>13.40.212</u>	AMD	<u>262</u>	6	<u>18.51.230</u>	AMD	<u>263</u>	2
<u>13.40.213</u>	AMD	<u>331</u>	6	<u>18.57.003</u>	AMD	<u>80</u>	14
<u>13.40.215</u>	AMD	<u>167</u>	7	<u>18.57A.010</u>	REP	<u>80</u>	59
<u>13.40.250</u>	REMD	<u>191</u>	5	<u>18.57A.020</u>	REP	<u>80</u>	59
<u>13.40.265</u>	AMD	<u>18</u>	11	<u>18.57A.023</u>	REP	<u>80</u>	59
<u>13.50.100</u>	AMD	<u>312</u>	121	<u>18.57A.025</u>	REP	<u>80</u>	59
<u>13.50.260</u>	AMD	<u>184</u>	1	<u>18.57A.030</u>	REP	<u>80</u>	59
<u>14.08.120</u>	AMD	<u>96</u>	1	<u>18.57A.035</u>	REP	<u>80</u>	59
<u>15</u>	ADD	<u>314</u>	1-7	<u>18.57A.040</u>	REP	<u>80</u>	59
<u>15.54.270</u>	REMD	<u>20</u>	1001	<u>18.57A.050</u>	REP	<u>80</u>	59
<u>15.54.325</u>	AMD	<u>20</u>	1002	<u>18.57A.060</u>	REP	<u>80</u>	59
<u>15.54.820</u>	AMD	<u>20</u>	1003	<u>18.57A.070</u>	REP	<u>80</u>	59
<u>15.58.040</u>	AMD	<u>180</u>	1	<u>18.57A.080</u>	REP	<u>80</u>	59
<u>15.58.240</u>	AMD	<u>180</u>	2	<u>18.57A.090</u>	REP	<u>80</u>	59
<u>15.83.010</u>	AMD	<u>176</u>	1	<u>18.57A.100</u>	REP	<u>80</u>	59
<u>15.83.020</u>	AMD	<u>176</u>	2	<u>18.57A.800</u>	REP	<u>80</u>	59
<u>15.83.030</u>	AMD	<u>176</u>	3	<u>18.57A.810</u>	REP	<u>80</u>	59
<u>16.08.030</u>	REP	<u>158</u>	10	<u>18.59.040</u>	AMD	<u>274</u>	2
<u>16.08.100</u>	AMD	<u>158</u>	1	<u>18.64</u>	ADD	<u>244</u>	2
<u>16.52.011</u>	AMD	<u>158</u>	2	<u>18.64</u>	ADD	<u>264</u>	1
<u>16.52.085</u>	AMD	<u>158</u>	3	<u>18.64.550</u>	AMD	<u>57</u>	27
<u>16.52.095</u>	AMD	<u>158</u>	4	<u>18.71</u>	ADD	<u>325</u>	1-3
<u>16.52.110</u>	REP	<u>158</u>	10	<u>18.71.051</u>	AMD	<u>325</u>	4
<u>16.52.165</u>	REP	<u>158</u>	10	<u>18.71.095</u>	AMD	<u>325</u>	5
<u>16.52.200</u>	AMD	<u>158</u>	5	<u>18.71A</u>	ADD	<u>80</u>	6
<u>16.52.205</u>	AMD	<u>158</u>	6				9-11
<u>16.52.207</u>	AMD	<u>158</u>	7	<u>18.71A.010</u>	REMD	<u>80</u>	2
<u>16.54.020</u>	AMD	<u>158</u>	8	<u>18.71A.020</u>	AMD	<u>80</u>	3
<u>16.54.030</u>	AMD	<u>158</u>	9	<u>18.71A.025</u>	AMD	<u>80</u>	4
<u>17.21.030</u>	AMD	<u>180</u>	3	<u>18.71A.030</u>	AMD	<u>80</u>	5
<u>17.21.134</u>	AMD	<u>180</u>	4	<u>18.71A.035</u>	REP	<u>80</u>	60
<u>17.28.257</u>	AMD	<u>54</u>	1	<u>18.71A.040</u>	REP	<u>80</u>	60
<u>18</u>	ADD	<u>241</u>	2-5	<u>18.71A.050</u>	AMD	<u>80</u>	7
			7	<u>18.71A.090</u>	AMD	<u>80</u>	8
<u>18.06.140</u>	AMD	<u>80</u>	13	<u>18.79.040</u>	AMD	<u>80</u>	15

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<u>18.79.060</u>	AMD	<u>80</u>	16	<u>19.16.100</u>	AMD	<u>30</u>	1
<u>18.79.240</u>	AMD	<u>80</u>	17	<u>19.16.260</u>	AMD	<u>30</u>	2
<u>18.79.260</u>	REMD	<u>80</u>	18	<u>19.16.440</u>	AMD	<u>30</u>	3
<u>18.79.270</u>	AMD	<u>80</u>	19	<u>19.16.450</u>	AMD	<u>30</u>	4
<u>18.83.110</u>	AMD	<u>302</u>	116	<u>19.27</u>	ADD	<u>43</u>	1
<u>18.89.020</u>	REMD	<u>80</u>	20	<u>19.27</u>	ADD	<u>88</u>	1-5
<u>18.92</u>	ADD	<u>229</u>	2	<u>19.28</u>	ADD	<u>211</u>	1
<u>18.100.050</u>	AMD	<u>80</u>	21	<u>19.28.041</u>	AMD	<u>153</u>	24
<u>18.106</u>	ADD	<u>153</u>	2-5	<u>19.28.051</u>	AMD	<u>153</u>	27
			28	<u>19.28.091</u>	REMD	<u>153</u>	23
<u>18.106.010</u>	AMD	<u>153</u>	1	<u>19.28.191</u>	AMD	<u>153</u>	25
<u>18.106.020</u>	AMD	<u>153</u>	6	<u>19.28.191</u>	AMD	<u>153</u>	26
<u>18.106.030</u>	AMD	<u>153</u>	7	<u>19.30.010</u>	REMD	<u>205</u>	1
<u>18.106.040</u>	AMD	<u>153</u>	8	<u>19.86</u>	ADD	<u>34</u>	3
<u>18.106.050</u>	AMD	<u>153</u>	9	<u>19.122.020</u>	REMD	<u>162</u>	1
<u>18.106.070</u>	AMD	<u>153</u>	10	<u>19.122.050</u>	AMD	<u>162</u>	2
<u>18.106.100</u>	AMD	<u>153</u>	11	<u>19.122.130</u>	AMD	<u>162</u>	3
<u>18.106.110</u>	AMD	<u>153</u>	12	<u>19.225.100</u>	AMD	<u>157</u>	1
<u>18.106.125</u>	AMD	<u>153</u>	13	<u>19.280</u>	ADD	<u>63</u>	2
<u>18.106.150</u>	AMD	<u>153</u>	14	<u>19.340.010</u>	REP	<u>240</u>	19
<u>18.106.180</u>	AMD	<u>153</u>	15	<u>19.340.020</u>	AMD	<u>240</u>	10
<u>18.106.200</u>	AMD	<u>153</u>	16	<u>19.340.020</u>	RECD	<u>240</u>	18
<u>18.106.220</u>	AMD	<u>153</u>	17	<u>19.340.030</u>	REP	<u>240</u>	19
<u>18.106.250</u>	AMD	<u>153</u>	18	<u>19.340.040</u>	AMD	<u>240</u>	11
<u>18.106.270</u>	AMD	<u>153</u>	19	<u>19.340.040</u>	RECD	<u>240</u>	18
<u>18.106.320</u>	AMD	<u>153</u>	20	<u>19.340.050</u>	RECD	<u>240</u>	18
<u>18.108.045</u>	AMD	<u>295</u>	1	<u>19.340.060</u>	RECD	<u>240</u>	18
<u>18.120.020</u>	AMD	<u>80</u>	22	<u>19.340.070</u>	AMD	<u>240</u>	12
<u>18.130</u>	ADD	<u>187</u>	1	<u>19.340.070</u>	RECD	<u>240</u>	18
<u>18.130.040</u>	REMD	<u>80</u>	23	<u>19.340.080</u>	AMD	<u>240</u>	13
<u>18.130.180</u>	AMD	<u>187</u>	2	<u>19.340.080</u>	RECD	<u>240</u>	18
<u>18.130.410</u>	AMD	<u>80</u>	24	<u>19.340.090</u>	AMD	<u>240</u>	14
<u>18.155</u>	ADD	<u>266</u>	5	<u>19.340.090</u>	RECD	<u>240</u>	18
<u>18.155.020</u>	AMD	<u>266</u>	1	<u>19.340.100</u>	AMD	<u>240</u>	15
<u>18.155.030</u>	AMD	<u>266</u>	2	<u>19.340.100</u>	RECD	<u>240</u>	18
<u>18.155.040</u>	AMD	<u>266</u>	7	<u>19.340.110</u>	AMD	<u>240</u>	16
<u>18.155.075</u>	AMD	<u>266</u>	3	<u>19.340.110</u>	RECD	<u>240</u>	18
<u>18.155.080</u>	AMD	<u>266</u>	4	<u>19.360.010</u>	REP	<u>57</u>	90
<u>18.155.900</u>	DECD	<u>266</u>	8	<u>19.360.020</u>	REP	<u>57</u>	90
<u>18.155.901</u>	DECD	<u>266</u>	8	<u>19.360.030</u>	REP	<u>57</u>	90
<u>18.155.902</u>	DECD	<u>266</u>	8	<u>19.360.040</u>	REP	<u>57</u>	90
<u>18.225.105</u>	AMD	<u>302</u>	115	<u>19.360.050</u>	REP	<u>57</u>	90
<u>18.250.010</u>	AMD	<u>80</u>	25	<u>19.360.060</u>	REP	<u>57</u>	90
<u>18.360.010</u>	REMD	<u>80</u>	26	<u>19.365.010</u>	REP	<u>240</u>	19
<u>19.02.075</u>	AMD	<u>164</u>	1	<u>19.400.010</u>	REP	<u>57</u>	90
<u>19.02.085</u>	AMD	<u>139</u>	3	<u>19.400.020</u>	REP	<u>57</u>	90
<u>19.09.020</u>	REMD	<u>57</u>	28	<u>19.400.030</u>	REP	<u>57</u>	90

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<u>19.405.020</u>	AMD	<u>20</u>	1004	<u>23B.06.260</u>	AMD	<u>57</u>	45
<u>20.01.330</u>	AMD	<u>274</u>	3	<u>23B.06.300</u>	AMD	<u>57</u>	46
<u>21.20.705</u>	REP	<u>151</u>	2	<u>23B.07.010</u>	AMD	<u>57</u>	47
<u>21.20.710</u>	REP	<u>151</u>	2	<u>23B.07.020</u>	AMD	<u>57</u>	48
<u>21.20.715</u>	REP	<u>151</u>	2	<u>23B.07.035</u>	AMD	<u>57</u>	49
<u>21.20.717</u>	REP	<u>151</u>	2	<u>23B.07.040</u>	AMD	<u>57</u>	50
<u>21.20.720</u>	REP	<u>151</u>	2	<u>23B.07.040</u>	AMD	<u>194</u>	15
<u>21.20.725</u>	REP	<u>151</u>	2	<u>23B.07.060</u>	AMD	<u>57</u>	51
<u>21.20.727</u>	REP	<u>151</u>	2	<u>23B.07.200</u>	AMD	<u>57</u>	52
<u>21.20.730</u>	REP	<u>151</u>	2	<u>23B.07.210</u>	AMD	<u>194</u>	13
<u>21.20.732</u>	REP	<u>151</u>	2	<u>23B.07.220</u>	AMD	<u>57</u>	53
<u>21.20.734</u>	REP	<u>151</u>	2	<u>23B.07.240</u>	AMD	<u>57</u>	54
<u>21.20.740</u>	REP	<u>151</u>	2	<u>23B.07.300</u>	AMD	<u>57</u>	55
<u>21.20.745</u>	REP	<u>151</u>	2	<u>23B.07.310</u>	AMD	<u>57</u>	56
<u>21.20.750</u>	REP	<u>151</u>	2	<u>23B.07.320</u>	AMD	<u>57</u>	57
<u>21.20.805</u>	REP	<u>151</u>	2	<u>23B.08</u>	ADD	<u>194</u>	1
<u>21.20.810</u>	AMD	<u>151</u>	1	<u>23B.08.030</u>	AMD	<u>194</u>	7
<u>21.20.815</u>	REP	<u>151</u>	2	<u>23B.08.070</u>	AMD	<u>57</u>	58
<u>21.20.820</u>	REP	<u>151</u>	2	<u>23B.08.210</u>	AMD	<u>57</u>	59
<u>21.20.825</u>	REP	<u>151</u>	2	<u>23B.08.230</u>	AMD	<u>57</u>	60
<u>21.20.830</u>	REP	<u>151</u>	2	<u>23B.08.240</u>	AMD	<u>57</u>	61
<u>21.20.835</u>	REP	<u>151</u>	2	<u>23B.08.430</u>	AMD	<u>57</u>	62
<u>21.20.840</u>	REP	<u>151</u>	2	<u>23B.08.530</u>	AMD	<u>57</u>	63
<u>21.20.845</u>	REP	<u>151</u>	2	<u>23B.08.735</u>	AMD	<u>194</u>	8
<u>21.20.850</u>	REP	<u>151</u>	2	<u>23B.09.020</u>	AMD	<u>57</u>	64
<u>23.95.105</u>	AMD	<u>57</u>	29	<u>23B.09.020</u>	AMD	<u>194</u>	9
<u>23.95.200</u>	AMD	<u>57</u>	30	<u>23B.09.030</u>	AMD	<u>57</u>	65
<u>23.95.265</u>	AMD	<u>57</u>	31	<u>23B.09.040</u>	AMD	<u>57</u>	66
<u>23.95.420</u>	AMD	<u>57</u>	32	<u>23B.09.060</u>	AMD	<u>57</u>	67
<u>23.95.450</u>	AMD	<u>57</u>	33	<u>23B.10.060</u>	AMD	<u>194</u>	10
<u>23B.01.200</u>	AMD	<u>57</u>	34	<u>23B.11.010</u>	AMD	<u>194</u>	11
<u>23B.01.200</u>	AMD	<u>194</u>	4	<u>23B.11.020</u>	AMD	<u>194</u>	12
<u>23B.01.230</u>	AMD	<u>57</u>	35	<u>23B.13.030</u>	AMD	<u>57</u>	68
<u>23B.01.240</u>	AMD	<u>57</u>	36	<u>23B.13.210</u>	AMD	<u>57</u>	69
<u>23B.01.250</u>	AMD	<u>57</u>	37	<u>23B.13.260</u>	AMD	<u>57</u>	70
<u>23B.01.290</u>	AMD	<u>57</u>	38	<u>23B.13.270</u>	AMD	<u>57</u>	71
<u>23B.01.400</u>	AMD	<u>57</u>	39	<u>23B.15.090</u>	AMD	<u>57</u>	72
<u>23B.01.410</u>	AMD	<u>57</u>	40	<u>23B.16.010</u>	AMD	<u>57</u>	73
<u>23B.01.420</u>	AMD	<u>57</u>	41	<u>23B.16.020</u>	AMD	<u>57</u>	74
<u>23B.02.020</u>	AMD	<u>194</u>	2	<u>23B.16.030</u>	REMD	<u>57</u>	75
<u>23B.02.050</u>	AMD	<u>57</u>	42	<u>23B.16.200</u>	AMD	<u>57</u>	76
<u>23B.02.060</u>	AMD	<u>194</u>	3	<u>23B.25.040</u>	AMD	<u>57</u>	77
<u>23B.06.010</u>	AMD	<u>194</u>	5	<u>23B.25.070</u>	AMD	<u>57</u>	78
<u>23B.06.030</u>	AMD	<u>194</u>	14	<u>23B.30.070</u>	AMD	<u>57</u>	79
<u>23B.06.200</u>	AMD	<u>57</u>	43	<u>24.03.005</u>	REMD	<u>57</u>	80
<u>23B.06.240</u>	AMD	<u>194</u>	6	<u>25.10.011</u>	AMD	<u>57</u>	81
<u>23B.06.250</u>	AMD	<u>57</u>	44	<u>25.15.006</u>	AMD	<u>57</u>	82

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<u>25.15.131</u>	AMD	<u>312</u>	727	<u>26.44.060</u>	AMD	<u>71</u>	2
<u>26</u>	ADD	<u>311</u>	1-7	<u>26.50.060</u>	AMD	<u>311</u>	9
<u>26.09</u>	ADD	<u>227</u>	4-8	<u>26.52.030</u>	AMD	<u>57</u>	83
			14	<u>27.04.045</u>	AMD	<u>274</u>	9
<u>26.09.015</u>	AMD	<u>29</u>	13	<u>27.12.222</u>	AMD	<u>207</u>	1
<u>26.09.170</u>	AMD	<u>227</u>	13	<u>27.15.020</u>	AMD	<u>207</u>	2
<u>26.09.191</u>	AMD	<u>311</u>	8	<u>27.34.070</u>	AMD	<u>48</u>	1
<u>26.10.010</u>	REP	<u>312</u>	905	<u>28A.150.260</u>	AMD	<u>61</u>	4
<u>26.10.015</u>	REP	<u>312</u>	905	<u>28A.150.260</u>	AMD	<u>288</u>	4
<u>26.10.020</u>	REP	<u>312</u>	905	<u>28A.150.390</u>	AMD	<u>90</u>	3
<u>26.10.030</u>	REP	<u>312</u>	905	<u>28A.155.065</u>	AMD	<u>90</u>	1
<u>26.10.032</u>	REP	<u>312</u>	905	<u>28A.155.065</u>	RECD	<u>90</u>	2
<u>26.10.034</u>	REP	<u>312</u>	905	<u>28A.160</u>	ADD	<u>339</u>	2
<u>26.10.040</u>	REP	<u>312</u>	905	<u>28A.160.010</u>	AMD	<u>339</u>	3
<u>26.10.045</u>	REP	<u>312</u>	905	<u>28A.175.010</u>	AMD	<u>61</u>	1
<u>26.10.050</u>	REP	<u>312</u>	905	<u>28A.175.075</u>	AMD	<u>114</u>	2
<u>26.10.060</u>	REP	<u>312</u>	905	<u>28A.195</u>	ADD	<u>167</u>	2
<u>26.10.070</u>	REP	<u>312</u>	905	<u>28A.210</u>	ADD	<u>39</u>	2
<u>26.10.080</u>	REP	<u>312</u>	905	<u>28A.210.090</u>	AMD	<u>80</u>	27
<u>26.10.090</u>	REP	<u>312</u>	905	<u>28A.225.225</u>	AMD	<u>90</u>	6
<u>26.10.100</u>	REP	<u>312</u>	905	<u>28A.225.270</u>	AMD	<u>90</u>	7
<u>26.10.110</u>	REP	<u>312</u>	905	<u>28A.225.330</u>	AMD	<u>167</u>	8
<u>26.10.120</u>	REP	<u>312</u>	905	<u>28A.230</u>	ADD	<u>307</u>	4
<u>26.10.130</u>	REP	<u>312</u>	905	<u>28A.230.090</u>	AMD	<u>7</u>	11
<u>26.10.135</u>	REP	<u>312</u>	905	<u>28A.230.090</u>	AMD	<u>307</u>	6
<u>26.10.140</u>	REP	<u>312</u>	905	<u>28A.230.094</u>	AMD	<u>208</u>	9
<u>26.10.150</u>	REP	<u>312</u>	905	<u>28A.230.215</u>	AMD	<u>307</u>	7
<u>26.10.160</u>	REP	<u>312</u>	905	<u>28A.235</u>	ADD	<u>288</u>	3
<u>26.10.170</u>	REP	<u>312</u>	905	<u>28A.235.290</u>	AMD	<u>288</u>	2
<u>26.10.180</u>	REP	<u>312</u>	905	<u>28A.300</u>	ADD	<u>233</u>	2
<u>26.10.190</u>	REP	<u>312</u>	905	<u>28A.300</u>	ADD	<u>251</u>	2
<u>26.10.200</u>	REP	<u>312</u>	905	<u>28A.300</u>	ADD	<u>292</u>	2
<u>26.10.210</u>	REP	<u>312</u>	905	<u>28A.300</u>	ADD	<u>307</u>	5
<u>26.10.220</u>	REP	<u>312</u>	905	<u>28A.300</u>	ADD	<u>353</u>	2
<u>26.10.910</u>	REP	<u>312</u>	905	<u>28A.300.112</u>	AMD	<u>59</u>	2
<u>26.19.011</u>	AMD	<u>227</u>	1	<u>28A.300.475</u>	AMD	<u>188</u>	1
<u>26.19.071</u>	AMD	<u>227</u>	2	<u>28A.300.507</u>	AMD	<u>61</u>	3
<u>26.23.035</u>	AMD	<u>349</u>	1	<u>28A.300.540</u>	AMD	<u>61</u>	2
<u>26.23.050</u>	AMD	<u>227</u>	9	<u>28A.300.615</u>	AMD	<u>8</u>	3
<u>26.23.060</u>	AMD	<u>125</u>	15	<u>28A.300.8001</u>	REP	<u>233</u>	4
<u>26.23.110</u>	AMD	<u>227</u>	15	<u>28A.300.801</u>	AMD	<u>114</u>	23
<u>26.33.350</u>	AMD	<u>274</u>	4	<u>28A.300.801</u>	RECD	<u>114</u>	24
<u>26.40.010</u>	AMD	<u>274</u>	5	<u>28A.320</u>	ADD	<u>13</u>	2-7
<u>26.40.020</u>	AMD	<u>274</u>	6	<u>28A.320</u>	ADD	<u>167</u>	1
<u>26.40.030</u>	AMD	<u>274</u>	7	<u>28A.320.128</u>	AMD	<u>167</u>	4
<u>26.44.015</u>	AMD	<u>274</u>	8	<u>28A.325.010</u>	AMD	<u>13</u>	9
<u>26.44.050</u>	AMD	<u>71</u>	1	<u>28A.325.050</u>	AMD	<u>13</u>	10

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<u>28A.335.040</u>	AMD	<u>40</u>	1	<u>28B.103.030</u>	AMD	<u>297</u>	3
<u>28A.345</u>	ADD	<u>235</u>	2	<u>28B.112</u>	ADD	<u>335</u>	2
<u>28A.400.210</u>	AMD	<u>18</u>	12				4-6
<u>28A.400.280</u>	AMD	<u>231</u>	1	<u>28B.145.050</u>	AMD	<u>357</u>	912
<u>28A.400.303</u>	AMD	<u>22</u>	1	<u>28B.155</u>	ADD	<u>306</u>	3
<u>28A.400.350</u>	AMD	<u>231</u>	2	<u>28C.18.200</u>	AMD	<u>2</u>	1
<u>28A.405.415</u>	AMD	<u>288</u>	5	<u>29A.04.061</u>	AMD	<u>208</u>	13
<u>28A.525</u>	ADD	<u>299</u>	1	<u>29A.04.216</u>	AMD	<u>337</u>	3
<u>28A.545</u>	ADD	<u>225</u>	2	<u>29A.04.410</u>	AMD	<u>337</u>	1
<u>28A.545.030</u>	AMD	<u>225</u>	1	<u>29A.04.420</u>	AMD	<u>337</u>	2
<u>28A.545.070</u>	AMD	<u>225</u>	3	<u>29A.04.430</u>	AMD	<u>337</u>	4
<u>28A.600</u>	ADD	<u>13</u>	8	<u>29A.08.110</u>	AMD	<u>208</u>	14
<u>28A.600</u>	ADD	<u>35</u>	2	<u>29A.08.140</u>	AMD	<u>208</u>	22
<u>28A.600</u>	ADD	<u>347</u>	1	<u>29A.08.170</u>	AMD	<u>208</u>	15
<u>28A.604</u>	ADD	<u>206</u>	1	<u>29A.08.172</u>	AMD	<u>208</u>	16
<u>28A.630</u>	ADD	<u>348</u>	2	<u>29A.08.174</u>	AMD	<u>208</u>	17
<u>28A.655.300</u>	AMD	<u>59</u>	1	<u>29A.08.210</u>	AMD	<u>208</u>	3
<u>28A.657.100</u>	AMD	<u>114</u>	3	<u>29A.08.230</u>	AMD	<u>208</u>	4
<u>28A.657.130</u>	REP	<u>114</u>	1	<u>29A.08.330</u>	AMD	<u>208</u>	5
<u>28A.710</u>	ADD	<u>167</u>	3	<u>29A.08.355</u>	AMD	<u>208</u>	7
<u>28A.710.050</u>	AMD	<u>49</u>	1	<u>29A.08.359</u>	AMD	<u>208</u>	18
<u>28A.710.070</u>	AMD	<u>49</u>	2	<u>29A.08.515</u>	AMD	<u>312</u>	728
<u>28A.710.160</u>	AMD	<u>49</u>	4	<u>29A.08.810</u>	AMD	<u>208</u>	6
<u>28A.710.250</u>	AMD	<u>49</u>	3	<u>29A.12</u>	ADD	<u>101</u>	2
<u>28A.710.900</u>	REP	<u>49</u>	5	<u>29A.12.070</u>	AMD	<u>101</u>	3
<u>28B</u>	ADD	<u>326</u>	1-5	<u>29A.32.031</u>	AMD	<u>208</u>	11
<u>28B.07.010</u>	AMD	<u>274</u>	10	<u>29A.32.210</u>	AMD	<u>337</u>	6
<u>28B.10</u>	ADD	<u>35</u>	4	<u>29A.32.241</u>	AMD	<u>208</u>	12
<u>28B.10</u>	ADD	<u>39</u>	3	<u>29A.40</u>	ADD	<u>208</u>	10
<u>28B.10</u>	ADD	<u>281</u>	2	<u>29A.40.091</u>	AMD	<u>12</u>	1
<u>28B.10.293</u>	AMD	<u>281</u>	1	<u>29A.64.081</u>	AMD	<u>337</u>	5
<u>28B.10.423</u>	AMD	<u>103</u>	2	<u>29A.80.041</u>	AMD	<u>208</u>	19
<u>28B.15.012</u>	AMD	<u>232</u>	1	<u>29A.84.140</u>	AMD	<u>208</u>	20
<u>28B.15.067</u>	AMD	<u>114</u>	4	<u>34.12.030</u>	AMD	<u>77</u>	5
<u>28B.20.830</u>	AMD	<u>92</u>	4	<u>34.12.100</u>	AMD	<u>77</u>	6
<u>28B.35</u>	ADD	<u>15</u>	1	<u>35</u>	ADD	<u>181</u>	1
<u>28B.50.248</u>	AMD	<u>279</u>	3	<u>35.20</u>	ADD	<u>37</u>	8
<u>28B.50.248</u>	AMD	<u>355</u>	4	<u>35.21.120</u>	AMD	<u>20</u>	1005
<u>28B.50.789</u>	AMD	<u>104</u>	1	<u>35.21.135</u>	AMD	<u>20</u>	1006
<u>28B.76.525</u>	AMD	<u>357</u>	910	<u>35.21.154</u>	AMD	<u>20</u>	1007
<u>28B.76.526</u>	AMD	<u>357</u>	911	<u>35.21.156</u>	AMD	<u>20</u>	1008
<u>28B.76.540</u>	AMD	<u>18</u>	26	<u>35.21.157</u>	AMD	<u>20</u>	1009
<u>28B.76.730</u>	AMD	<u>259</u>	1	<u>35.21.409</u>	AMD	<u>20</u>	1010
<u>28B.77</u>	ADD	<u>307</u>	2,3	<u>35.21.755</u>	AMD	<u>20</u>	1011
<u>28B.95.170</u>	REP	<u>114</u>	1	<u>35.21.915</u>	AMD	<u>223</u>	3
<u>28B.103.010</u>	AMD	<u>297</u>	1	<u>35.22.625</u>	AMD	<u>20</u>	1012
<u>28B.103.020</u>	AMD	<u>297</u>	2	<u>35.23.351</u>	AMD	<u>20</u>	1013

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<u>35.58.240</u>	AMD	<u>274</u>	11	<u>36.70A.270</u>	AMD	<u>214</u>	4
<u>35.61.150</u>	AMD	<u>83</u>	1	<u>36.70A.600</u>	AMD	<u>173</u>	1
<u>35.68.075</u>	AMD	<u>274</u>	12	<u>36.70A.610</u>	AMD	<u>173</u>	6
<u>35.86A.010</u>	AMD	<u>274</u>	13	<u>36.70A.620</u>	AMD	<u>173</u>	3
<u>35.86A.070</u>	AMD	<u>274</u>	14	<u>36.87.130</u>	AMD	<u>300</u>	1
<u>35.90.020</u>	AMD	<u>139</u>	59	<u>36.93.090</u>	AMD	<u>20</u>	1028
<u>35.92</u>	ADD	<u>135</u>	1	<u>36.94.010</u>	AMD	<u>20</u>	1029
<u>35.92.020</u>	AMD	<u>20</u>	1014	<u>36.96</u>	ADD	<u>179</u>	5
<u>35.92.060</u>	AMD	<u>274</u>	15	<u>36.96.010</u>	AMD	<u>179</u>	2
<u>35.94.040</u>	AMD	<u>31</u>	1	<u>36.96.030</u>	AMD	<u>179</u>	3
<u>35.94.050</u>	AMD	<u>20</u>	1015	<u>36.96.070</u>	AMD	<u>179</u>	4
<u>35.95A.050</u>	AMD	<u>274</u>	16	<u>36.160.020</u>	AMD	<u>192</u>	1
<u>35.101</u>	ADD	<u>215</u>	2,4	<u>36.160.100</u>	AMD	<u>192</u>	2
<u>35.101.010</u>	AMD	<u>215</u>	1	<u>36.160.110</u>	AMD	<u>192</u>	3
<u>35.101.130</u>	AMD	<u>215</u>	3	<u>38.52.105</u>	AMD	<u>7</u>	6
<u>35.103</u>	ADD	<u>198</u>	2	<u>38.52.430</u>	AMD	<u>330</u>	4
<u>35A.14</u>	ADD	<u>142</u>	2	<u>39.04.310</u>	AMD	<u>255</u>	1
<u>35A.21.152</u>	AMD	<u>20</u>	1016	<u>39.04.350</u>	AMD	<u>255</u>	2
<u>35A.21.153</u>	AMD	<u>20</u>	1017	<u>39.23.005</u>	AMD	<u>274</u>	19
<u>35A.21.324</u>	AMD	<u>20</u>	1018	<u>39.26</u>	ADD	<u>269</u>	5
<u>35A.21.360</u>	AMD	<u>223</u>	4	<u>39.26.180</u>	AMD	<u>269</u>	4
<u>36</u>	ADD	<u>27</u>	1-13	<u>39.26.200</u>	AMD	<u>269</u>	3
<u>36.01.290</u>	AMD	<u>223</u>	2	<u>39.30.060</u>	AMD	<u>140</u>	1
<u>36.27</u>	ADD	<u>203</u>	2	<u>39.35D</u>	ADD	<u>9</u>	5
<u>36.28A</u>	ADD	<u>45</u>	4	<u>39.89.020</u>	AMD	<u>280</u>	1
<u>36.28A.400</u>	REP	<u>28</u>	9	<u>39.102.020</u>	AMD	<u>280</u>	2
<u>36.28A.405</u>	AMD	<u>28</u>	7	<u>39.104.020</u>	AMD	<u>280</u>	3
<u>36.28A.420</u>	AMD	<u>28</u>	8	<u>41.04</u>	ADD	<u>6</u>	3
<u>36.32.120</u>	AMD	<u>20</u>	1019	<u>41.04</u>	ADD	<u>323</u>	1
<u>36.32.304</u>	AMD	<u>20</u>	1020	<u>41.04.005</u>	AMD	<u>178</u>	1
<u>36.34.192</u>	AMD	<u>20</u>	1021	<u>41.04.655</u>	AMD	<u>6</u>	1
<u>36.57.040</u>	AMD	<u>274</u>	17	<u>41.04.655</u>	AMD	<u>29</u>	14
<u>36.57A.050</u>	AMD	<u>83</u>	2	<u>41.04.665</u>	AMD	<u>6</u>	2
<u>36.57A.090</u>	AMD	<u>274</u>	18	<u>41.05</u>	ADD	<u>8</u>	4,5
<u>36.58.040</u>	AMD	<u>20</u>	1022	<u>41.05</u>	ADD	<u>231</u>	3
<u>36.58.045</u>	AMD	<u>20</u>	1023	<u>41.05</u>	ADD	<u>245</u>	2
<u>36.58.050</u>	AMD	<u>20</u>	1024	<u>41.05</u>	ADD	<u>345</u>	2
<u>36.58A.010</u>	AMD	<u>20</u>	1025	<u>41.05</u>	ADD	<u>346</u>	6
<u>36.70A</u>	ADD	<u>217</u>	2-5	<u>41.05.014</u>	AMD	<u>57</u>	84
<u>36.70A.030</u>	REMD	<u>173</u>	4	<u>41.05.095</u>	AMD	<u>274</u>	20
<u>36.70A.130</u>	AMD	<u>20</u>	1026	<u>41.05.175</u>	AMD	<u>18</u>	13
<u>36.70A.130</u>	AMD	<u>113</u>	1	<u>41.05.600</u>	AMD	<u>228</u>	1
<u>36.70A.200</u>	AMD	<u>20</u>	1027	<u>41.05.700</u>	AMD	<u>92</u>	2
<u>36.70A.200</u>	AMD	<u>128</u>	1	<u>41.06</u>	ADD	<u>305</u>	1
<u>36.70A.250</u>	AMD	<u>214</u>	1	<u>41.06.142</u>	AMD	<u>269</u>	2
<u>36.70A.252</u>	AMD	<u>214</u>	2	<u>41.14.060</u>	AMD	<u>14</u>	1
<u>36.70A.260</u>	AMD	<u>214</u>	3	<u>41.14.130</u>	AMD	<u>14</u>	2

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41.16.010	AMD	107	1	42.56.250	REMD	106	1
41.16.020	AMD	107	2	42.56.270	REMD	238	11
41.18.010	REMD	107	3	42.56.360	AMD	323	2
41.18.015	AMD	107	4	42.56.400	AMD	240	9
41.20.010	AMD	107	5	42.56.400	AMD	243	4
41.24.030	AMD	144	1	42.56.590	AMD	65	1
41.24.170	AMD	144	2	43	ADD	257	1-9
41.26.030	AMD	107	6				11,12
41.26.110	REMD	107	7	43	ADD	313	2-6
41.26.460	AMD	161	1	43	ADD	332	1-5
41.32.4992	AMD	329	2				8,9
41.32.785	AMD	161	2	43.06	ADD	132	1,2
41.32.851	AMD	161	3	43.09.025	AMD	18	14
41.34.060	AMD	160	2	43.09.230	AMD	179	1
41.34.140	AMD	160	3	43.10.005	AMD	111	1
41.35.220	AMD	161	4	43.15	ADD	114	24
41.37.010	AMD	108	1	43.15.020	AMD	114	5
41.37.170	AMD	161	5	43.15.030	AMD	114	18
41.40.1987	AMD	329	1	43.15.040	AMD	114	19
41.40.660	AMD	161	6	43.15.060	AMD	114	20
41.40.845	AMD	161	7	43.15.065	AMD	114	21
41.45.050	AMD	103	3	43.15.070	AMD	114	22
41.45.060	AMD	103	4	43.19.501	AMD	356	7005
41.50	ADD	103	6	43.19A	ADD	290	2,3
41.50.075	AMD	103	5	43.20A	ADD	201	2
41.50.770	AMD	160	4	43.20A.635	AMD	274	21
41.50.780	AMD	160	5	43.20A.710	AMD	270	10
41.56	ADD	298	2,3	43.21A	ADD	221	2-4
41.56.030	AMD	289	1	43.21A.020	AMD	20	1030
41.56.030	AMD	298	1	43.21A.175	AMD	20	1031
41.56.510	AMD	289	2	43.21A.702	AMD	20	1032
41.80	ADD	77	2	43.21A.711	AMD	20	1033
41.80.005	REMD	77	3	43.21A.731	AMD	17	1
41.80.010	REMD	77	4	43.21B.005	AMD	214	5
41.80.040	AMD	357	913	43.21B.110	REMD	20	1034
41.80.200	AMD	89	1	43.21B.110	REMD	20	1035
42.17A	ADD	152	9-11	43.21B.110	REMD	138	10
42.17A.005	AMD	152	2	43.21B.110	REMD	138	11
42.17A.240	AMD	152	3	43.21B.130	AMD	20	1036
42.17A.250	AMD	152	4	43.21B.260	AMD	20	1037
42.17A.255	AMD	152	5	43.21B.300	AMD	20	1038
42.17A.260	AMD	152	6	43.21C.036	AMD	20	1039
42.17A.265	AMD	152	7	43.21C.0381	AMD	20	1040
42.17A.305	AMD	152	8	43.21C.210	AMD	20	1041
42.23.030	AMD	69	1	43.21C.229	AMD	87	1
42.56	ADD	167	10	43.21C.495	AMD	173	2
42.56	ADD	335	7	43.21F.090	AMD	20	1042

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43.21M.010	RECD	20	2001	43.43.832	AMD	270	7
43.21M.020	RECD	20	2001	43.43.944	AMD	88	6
43.21M.030	RECD	20	2001	43.59	ADD	72	1
43.21M.040	RECD	20	2001	43.59.155	REP	72	2
43.21M.900	RECD	20	2001	43.60A	ADD	56	2
43.24.090	AMD	274	22	43.60A	ADD	328	1
43.27A.190	AMD	20	1043	43.62	ADD	34	1
43.31.502	AMD	357	914	43.63A.670	AMD	274	24
43.31.512	AMD	274	23	43.63A.750	AMD	356	7008
43.31.605	AMD	169	2	43.70	ADD	102	4
43.31.605	AMD	315	8	43.70	ADD	115	4,5
43.37.010	RECD	20	2002	43.70	ADD	347	2
43.37.030	RECD	20	2002	43.70.110	REMD	80	28
43.37.040	RECD	20	2002	43.70.220	AMD	80	29
43.37.050	AMD	20	1044	43.70.442	REMD	80	30
43.37.050	RECD	20	2002	43.70.442	REMD	229	1
43.37.060	RECD	20	2002	43.70.470	AMD	80	31
43.37.080	AMD	20	1045	43.70.495	AMD	147	1
43.37.080	RECD	20	2002	43.70.700	AMD	68	2
43.37.090	RECD	20	2002	43.71	ADD	283	2
43.37.100	RECD	20	2002	43.71B.010	AMD	256	102
43.37.110	AMD	20	1046	43.71B.901	AMD	256	101
43.37.110	RECD	20	2002	43.79	ADD	357	924
43.37.120	RECD	20	2002	43.79.195	AMD	2	2
43.37.130	RECD	20	2002	43.79A.040	REMD	18	2
43.37.140	AMD	20	1047	43.83B	ADD	168	1,7,8
43.37.140	RECD	20	2002	43.83B.005	DECD	168	9
43.37.150	RECD	20	2002	43.83B.200	DECD	168	9
43.37.160	RECD	20	2002	43.83B.210	DECD	168	9
43.37.170	AMD	20	1048	43.83B.220	REP	168	10
43.37.170	RECD	20	2002	43.83B.300	DECD	168	9
43.37.180	RECD	20	2002	43.83B.336	REP	168	10
43.37.190	RECD	20	2002	43.83B.345	DECD	168	9
43.37.200	RECD	20	2002	43.83B.360	DECD	168	9
43.37.210	RECD	20	2002	43.83B.380	DECD	168	9
43.37.215	RECD	20	2002	43.83B.385	DECD	168	9
43.37.220	AMD	20	1049	43.83B.400	AMD	168	2
43.37.220	RECD	20	2002	43.83B.405	AMD	168	3
43.37.910	RECD	20	2002	43.83B.410	AMD	168	4
43.41.270	AMD	20	1050	43.83B.415	AMD	168	5
43.43	ADD	28	1-3	43.83B.430	AMD	168	6
43.43.120	AMD	97	2	43.84.092	REMD	18	3
43.43.271	AMD	161	8	43.84.092	REMD	103	7
43.43.3951	REP	330	19	43.84.092	REMD	148	3
43.43.545	AMD	26	6	43.84.092	REMD	221	5
43.43.754	AMD	26	7	43.84.092	REMD	354	11
43.43.823	AMD	28	6	43.88.030	AMD	218	1

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<u>43.88.055</u>	AMD	<u>218</u>	2	<u>43.200.170</u>	AMD	<u>20</u>	1056
<u>43.88C.010</u>	AMD	<u>352</u>	1	<u>43.200.170</u>	RECD	<u>20</u>	2005
<u>43.101</u>	ADD	<u>26</u>	8	<u>43.200.180</u>	AMD	<u>20</u>	1057
<u>43.101</u>	ADD	<u>119</u>	3-9	<u>43.200.180</u>	RECD	<u>20</u>	2005
			11	<u>43.200.190</u>	RECD	<u>20</u>	2005
<u>43.101</u>	ADD	<u>294</u>	1	<u>43.200.200</u>	RECD	<u>20</u>	2005
<u>43.101.010</u>	AMD	<u>119</u>	2	<u>43.200.220</u>	AMD	<u>20</u>	1058
<u>43.101.030</u>	AMD	<u>44</u>	1	<u>43.200.220</u>	RECD	<u>20</u>	2005
<u>43.101.080</u>	AMD	<u>119</u>	13	<u>43.200.230</u>	AMD	<u>20</u>	1059
<u>43.101.085</u>	AMD	<u>119</u>	1	<u>43.200.230</u>	RECD	<u>20</u>	2005
<u>43.101.220</u>	AMD	<u>119</u>	14	<u>43.200.233</u>	AMD	<u>20</u>	1060
<u>43.101.380</u>	AMD	<u>119</u>	10	<u>43.200.233</u>	RECD	<u>20</u>	2005
<u>43.101.400</u>	AMD	<u>119</u>	12	<u>43.200.235</u>	AMD	<u>20</u>	1061
<u>43.131.394</u>	AMD	<u>20</u>	1051	<u>43.200.235</u>	RECD	<u>20</u>	2005
<u>43.131.417</u>	AMD	<u>306</u>	1	<u>43.200.900</u>	RECD	<u>20</u>	2005
<u>43.131.418</u>	AMD	<u>306</u>	2	<u>43.200.901</u>	RECD	<u>20</u>	2005
<u>43.135.010</u>	REP	<u>218</u>	6	<u>43.200.905</u>	AMD	<u>20</u>	1062
<u>43.135.025</u>	AMD	<u>218</u>	3	<u>43.200.905</u>	RECD	<u>20</u>	2005
<u>43.135.034</u>	AMD	<u>218</u>	4	<u>43.200.907</u>	AMD	<u>20</u>	1063
<u>43.135.0341</u>	REP	<u>218</u>	6	<u>43.200.907</u>	RECD	<u>20</u>	2005
<u>43.135.0342</u>	REP	<u>218</u>	6	<u>43.205.010</u>	RECD	<u>20</u>	2006
<u>43.135.0343</u>	REP	<u>218</u>	6	<u>43.205.020</u>	RECD	<u>20</u>	2006
<u>43.135.0351</u>	REP	<u>218</u>	6	<u>43.216</u>	ADD	<u>90</u>	2
<u>43.135.080</u>	REP	<u>218</u>	6	<u>43.216</u>	ADD	<u>124</u>	1
<u>43.135.904</u>	REP	<u>218</u>	6	<u>43.216</u>	ADD	<u>189</u>	2,3
<u>43.145.010</u>	RECD	<u>20</u>	2003	<u>43.216</u>	ADD	<u>279</u>	2
<u>43.145.020</u>	RECD	<u>20</u>	2003	<u>43.216</u>	ADD	<u>339</u>	1
<u>43.145.030</u>	RECD	<u>20</u>	2003	<u>43.216</u>	ADD	<u>342</u>	2
<u>43.146.010</u>	RECD	<u>20</u>	2004	<u>43.216</u>	ADD	<u>355</u>	3
<u>43.146.900</u>	AMD	<u>20</u>	1052	<u>43.216.010</u>	AMD	<u>270</u>	11
<u>43.146.900</u>	RECD	<u>20</u>	2004	<u>43.216.015</u>	AMD	<u>90</u>	9
<u>43.150.050</u>	AMD	<u>76</u>	20	<u>43.216.015</u>	AMD	<u>262</u>	1
<u>43.157.010</u>	AMD	<u>46</u>	2	<u>43.216.020</u>	AMD	<u>90</u>	4
<u>43.157.020</u>	AMD	<u>46</u>	3	<u>43.216.020</u>	AMD	<u>262</u>	5
<u>43.180.070</u>	REMD	<u>274</u>	25	<u>43.216.075</u>	AMD	<u>262</u>	4
<u>43.185C.060</u>	AMD	<u>357</u>	915	<u>43.216.089</u>	AMD	<u>262</u>	3
<u>43.185C.210</u>	AMD	<u>155</u>	1	<u>43.216.135</u>	REMD	<u>279</u>	1
<u>43.185C.260</u>	AMD	<u>331</u>	8	<u>43.216.135</u>	REMD	<u>321</u>	2
<u>43.200.010</u>	RECD	<u>20</u>	2005	<u>43.216.135</u>	REMD	<u>355</u>	2
<u>43.200.015</u>	REMD	<u>20</u>	1053	<u>43.216.270</u>	REMD	<u>270</u>	9
<u>43.200.015</u>	RECD	<u>20</u>	2005	<u>43.216.305</u>	AMD	<u>343</u>	5
<u>43.200.020</u>	RECD	<u>20</u>	2005	<u>43.216.514</u>	AMD	<u>343</u>	3
<u>43.200.030</u>	RECD	<u>20</u>	2005	<u>43.216.515</u>	AMD	<u>321</u>	1
<u>43.200.070</u>	AMD	<u>20</u>	1054	<u>43.216.572</u>	AMD	<u>114</u>	6
<u>43.200.070</u>	RECD	<u>20</u>	2005	<u>43.216.574</u>	AMD	<u>114</u>	7
<u>43.200.080</u>	AMD	<u>20</u>	1055	<u>43.216.576</u>	AMD	<u>90</u>	5
<u>43.200.080</u>	RECD	<u>20</u>	2005	<u>43.216.720</u>	AMD	<u>274</u>	26

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<u>43.220.070</u>	AMD	<u>274</u>	27	<u>46.20.156</u>	AMD	<u>208</u>	21
<u>43.330</u>	ADD	<u>236</u>	3	<u>46.20.161</u>	AMD	<u>261</u>	3
<u>43.330.300</u>	AMD	<u>60</u>	1	<u>46.20.245</u>	AMD	<u>330</u>	5
<u>43.371.060</u>	AMD	<u>131</u>	1	<u>46.20.285</u>	AMD	<u>16</u>	1
<u>44.04.325</u>	AMD	<u>114</u>	8	<u>46.20.3101</u>	AMD	<u>330</u>	6
<u>44.55.010</u>	REP	<u>114</u>	1	<u>46.20.311</u>	AMD	<u>330</u>	7
<u>44.55.020</u>	REP	<u>114</u>	1	<u>46.20.355</u>	REMD	<u>330</u>	8
<u>44.55.030</u>	REP	<u>114</u>	1	<u>46.20.385</u>	AMD	<u>330</u>	9
<u>44.55.040</u>	REP	<u>114</u>	1	<u>46.20.720</u>	AMD	<u>330</u>	10
<u>44.55.050</u>	REP	<u>114</u>	1	<u>46.20.740</u>	AMD	<u>330</u>	11
<u>44.55.060</u>	REP	<u>114</u>	1	<u>46.20.750</u>	AMD	<u>330</u>	12
<u>44.68.010</u>	AMD	<u>114</u>	9	<u>46.25.060</u>	AMD	<u>78</u>	2
<u>44.68.020</u>	REP	<u>114</u>	1	<u>46.30</u>	ADD	<u>182</u>	1
<u>44.68.035</u>	REP	<u>114</u>	1	<u>46.37.190</u>	AMD	<u>95</u>	1
<u>44.68.040</u>	AMD	<u>114</u>	10	<u>46.44.030</u>	AMD	<u>110</u>	1
<u>44.68.050</u>	AMD	<u>114</u>	11	<u>46.55.113</u>	AMD	<u>117</u>	2
<u>44.68.060</u>	AMD	<u>114</u>	12	<u>46.55.113</u>	AMD	<u>330</u>	13
<u>44.68.065</u>	AMD	<u>114</u>	13	<u>46.55.350</u>	REP	<u>117</u>	4
<u>44.68.085</u>	AMD	<u>114</u>	14	<u>46.55.360</u>	AMD	<u>117</u>	3
<u>44.68.090</u>	AMD	<u>114</u>	15	<u>46.61.050</u>	AMD	<u>66</u>	1
<u>44.68.100</u>	AMD	<u>114</u>	16	<u>46.61.140</u>	AMD	<u>199</u>	2
<u>44.68.105</u>	AMD	<u>114</u>	17	<u>46.61.190</u>	AMD	<u>66</u>	2
<u>46</u>	ADD	<u>182</u>	2	<u>46.61.200</u>	AMD	<u>66</u>	3
<u>46.04</u>	ADD	<u>11</u>	2	<u>46.61.500</u>	AMD	<u>330</u>	14
<u>46.04</u>	ADD	<u>93</u>	4	<u>46.61.5055</u>	AMD	<u>330</u>	15
<u>46.04</u>	ADD	<u>129</u>	4	<u>46.61.5056</u>	AMD	<u>330</u>	16
<u>46.04</u>	ADD	<u>199</u>	1	<u>46.61.506</u>	AMD	<u>80</u>	33
<u>46.16A</u>	ADD	<u>118</u>	1	<u>46.61.508</u>	AMD	<u>80</u>	34
<u>46.17</u>	ADD	<u>1</u>	2	<u>46.61.524</u>	AMD	<u>330</u>	17
<u>46.17.120</u>	AMD	<u>239</u>	1	<u>46.61.526</u>	AMD	<u>146</u>	1
<u>46.17.220</u>	REMD	<u>93</u>	2	<u>46.61.575</u>	AMD	<u>163</u>	1
<u>46.17.220</u>	REMD	<u>129</u>	1	<u>46.61.755</u>	AMD	<u>66</u>	4
<u>46.17.323</u>	AMD	<u>1</u>	5	<u>46.63.170</u>	AMD	<u>224</u>	1
<u>46.17.350</u>	AMD	<u>1</u>	3	<u>46.68</u>	ADD	<u>224</u>	2
<u>46.17.355</u>	AMD	<u>1</u>	4	<u>46.68.041</u>	AMD	<u>330</u>	18
<u>46.17.365</u>	REP	<u>1</u>	6	<u>46.68.290</u>	AMD	<u>219</u>	705
<u>46.18.200</u>	REMD	<u>93</u>	1	<u>46.68.310</u>	AMD	<u>219</u>	702
<u>46.18.200</u>	REMD	<u>129</u>	2	<u>46.68.395</u>	AMD	<u>219</u>	707
<u>46.18.255</u>	AMD	<u>18</u>	15	<u>46.68.415</u>	REP	<u>1</u>	6
<u>46.18.265</u>	AMD	<u>18</u>	16	<u>46.68.420</u>	REMD	<u>93</u>	3
<u>46.18.285</u>	AMD	<u>18</u>	17	<u>46.68.420</u>	REMD	<u>129</u>	3
<u>46.18.290</u>	AMD	<u>18</u>	18	<u>46.68.435</u>	AMD	<u>148</u>	4
<u>46.19.010</u>	AMD	<u>80</u>	32	<u>46.72.010</u>	AMD	<u>274</u>	28
<u>46.20</u>	ADD	<u>124</u>	3	<u>46.96</u>	ADD	<u>174</u>	1
<u>46.20.117</u>	AMD	<u>124</u>	2	<u>47.04.170</u>	AMD	<u>274</u>	29
<u>46.20.117</u>	AMD	<u>261</u>	2	<u>47.10.873</u>	AMD	<u>24</u>	1
<u>46.20.155</u>	AMD	<u>208</u>	8	<u>47.36.110</u>	AMD	<u>66</u>	5

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47.66.110	AMD	219	704	48.44.200	AMD	274	35
47.68.250	AMD	304	3	48.44.210	AMD	274	36
48	ADD	240	1-5	48.44.220	AMD	274	37
48.02.120	AMD	240	7	48.44.315	AMD	245	5
48.02.190	AMD	195	2	48.44.315	AMD	346	9
48.02.220	AMD	240	8	48.44.323	AMD	18	21
48.14.040	AMD	195	3	48.44.341	AMD	228	5
48.18.550	AMD	29	15	48.46.272	AMD	245	6
48.20.389	AMD	18	19	48.46.272	AMD	346	10
48.20.391	AMD	245	3	48.46.274	AMD	18	22
48.20.391	AMD	346	7	48.46.291	AMD	228	6
48.20.420	AMD	274	30	48.46.320	AMD	274	38
48.20.580	AMD	228	2	48.46.370	AMD	274	39
48.21.143	AMD	245	4	49	ADD	309	1-6
48.21.143	AMD	346	8				8
48.21.150	AMD	274	31	49.12	ADD	212	1
48.21.223	AMD	18	20	49.12.110	AMD	274	40
48.21.241	AMD	228	3	49.17	ADD	100	4
48.23.525	AMD	197	4	49.44.180	AMD	76	18
48.30.140	AMD	197	1	49.46.010	REMD	212	3
48.30.150	AMD	197	2	49.48.010	AMD	84	3
48.30.155	AMD	197	3	49.48.150	AMD	84	1
48.30.300	AMD	228	8	49.48.160	AMD	84	2
48.30.300	AMD	274	32	49.60	ADD	52	3
48.30.320	AMD	274	33	49.60.010	AMD	52	1
48.31B	ADD	243	3	49.60.020	AMD	52	2
48.31B.005	AMD	243	1	49.60.030	AMD	52	4
48.31B.038	AMD	243	2	49.60.040	AMD	85	1
48.41.140	AMD	274	34	49.60.120	AMD	52	5
48.41.220	AMD	228	4	49.60.130	AMD	52	6
48.42.100	AMD	80	35	49.60.172	AMD	76	19
48.43	ADD	4	2	49.60.175	AMD	52	7
48.43	ADD	116	1	49.60.176	AMD	52	8
48.43	ADD	240	6	49.60.178	AMD	52	9
48.43	ADD	245	1	49.60.180	AMD	52	10
48.43	ADD	247	1	49.60.190	AMD	52	11
48.43	ADD	283	1	49.60.200	AMD	52	12
48.43	ADD	316	1	49.60.215	AMD	52	13
48.43	ADD	345	3	49.60.222	AMD	52	14
48.43	ADD	346	5	49.60.223	AMD	52	15
48.43.005	REMD	196	1	49.60.224	AMD	52	16
48.43.0128	AMD	228	9	49.60.225	AMD	52	17
48.43.016	AMD	193	2	49.60.230	AMD	145	1
48.43.094	AMD	80	36	49.60.510	AMD	254	1
48.43.115	AMD	80	37	49.74.005	AMD	274	41
48.43.735	AMD	92	1	50.04	ADD	212	2
48.43.750	AMD	4	1	50.12.070	AMD	334	2

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<u>50.12.200</u>	AMD	<u>86</u>	1	<u>51.48.017</u>	AMD	<u>277</u>	2
<u>50.12.210</u>	AMD	<u>274</u>	42	<u>51.48.030</u>	AMD	<u>277</u>	3
<u>50.12.220</u>	AMD	<u>334</u>	3	<u>51.48.040</u>	AMD	<u>277</u>	4
<u>50.16</u>	ADD	<u>7</u>	4	<u>51.48.060</u>	AMD	<u>277</u>	5
<u>50.20.010</u>	AMD	<u>7</u>	8	<u>51.48.080</u>	AMD	<u>277</u>	6
<u>50.20.190</u>	AMD	<u>86</u>	2	<u>52.02.020</u>	AMD	<u>94</u>	1
<u>50.29</u>	ADD	<u>7</u>	5	<u>52.18.050</u>	AMD	<u>99</u>	1
<u>50.29.020</u>	REP	<u>86</u>	6	<u>52.26.220</u>	AMD	<u>99</u>	2
<u>50.29.021</u>	AMD	<u>86</u>	3	<u>52.30</u>	ADD	<u>198</u>	1
<u>50.50.070</u>	AMD	<u>86</u>	4	<u>53.12.260</u>	AMD	<u>83</u>	3
<u>50A.05</u>	ADD	<u>125</u>	18	<u>53.54.010</u>	AMD	<u>105</u>	1
<u>50A.05.010</u>	AMD	<u>125</u>	1	<u>53.54.020</u>	AMD	<u>105</u>	2
<u>50A.05.070</u>	AMD	<u>86</u>	5	<u>53.54.030</u>	AMD	<u>105</u>	3
<u>50A.10.010</u>	AMD	<u>125</u>	2	<u>53.54.030</u>	AMD	<u>112</u>	1
<u>50A.10.040</u>	AMD	<u>125</u>	3	<u>54.12.080</u>	AMD	<u>83</u>	4
<u>50A.15.020</u>	AMD	<u>125</u>	4	<u>57.08.014</u>	AMD	<u>274</u>	44
<u>50A.15.060</u>	AMD	<u>125</u>	5	<u>57.12.010</u>	AMD	<u>83</u>	5
<u>50A.15.080</u>	AMD	<u>125</u>	6	<u>58.09.050</u>	AMD	<u>57</u>	85
<u>50A.15.100</u>	AMD	<u>125</u>	7	<u>58.09.110</u>	AMD	<u>57</u>	86
<u>50A.25.070</u>	AMD	<u>125</u>	8	<u>58.20</u>	ADD	<u>50</u>	1,2,4,6
<u>50A.30.010</u>	AMD	<u>125</u>	9				8,12
<u>50A.30.035</u>	AMD	<u>125</u>	10	<u>58.20.110</u>	REP	<u>50</u>	13
<u>50A.40</u>	ADD	<u>125</u>	16,17	<u>58.20.120</u>	REP	<u>50</u>	13
<u>50A.40.010</u>	AMD	<u>125</u>	11	<u>58.20.130</u>	REP	<u>50</u>	13
<u>50A.40.020</u>	AMD	<u>125</u>	12	<u>58.20.140</u>	AMD	<u>50</u>	3
<u>50A.40.030</u>	AMD	<u>125</u>	13	<u>58.20.150</u>	REP	<u>50</u>	13
<u>50A.50.010</u>	AMD	<u>125</u>	14	<u>58.20.160</u>	AMD	<u>50</u>	5
<u>50B.04</u>	ADD	<u>98</u>	7	<u>58.20.170</u>	REP	<u>50</u>	13
<u>50B.04.010</u>	AMD	<u>98</u>	1	<u>58.20.180</u>	AMD	<u>50</u>	7
<u>50B.04.020</u>	AMD	<u>98</u>	2	<u>58.20.190</u>	REP	<u>50</u>	13
<u>50B.04.050</u>	AMD	<u>98</u>	3	<u>58.20.200</u>	AMD	<u>50</u>	9
<u>50B.04.080</u>	AMD	<u>98</u>	4	<u>58.20.210</u>	AMD	<u>50</u>	10
<u>50B.04.090</u>	AMD	<u>98</u>	5	<u>58.20.220</u>	AMD	<u>50</u>	11
<u>50B.04.120</u>	AMD	<u>98</u>	6	<u>59.18</u>	ADD	<u>169</u>	1
<u>51.04</u>	ADD	<u>165</u>	4	<u>59.18.057</u>	AMD	<u>315</u>	2
<u>51.04.030</u>	AMD	<u>80</u>	38	<u>59.18.063</u>	AMD	<u>315</u>	3
<u>51.08</u>	ADD	<u>213</u>	1	<u>59.18.170</u>	AMD	<u>177</u>	1
<u>51.08.030</u>	AMD	<u>274</u>	43	<u>59.18.230</u>	AMD	<u>177</u>	2
<u>51.08.142</u>	AMD	<u>234</u>	1	<u>59.18.230</u>	AMD	<u>315</u>	6
<u>51.14</u>	ADD	<u>277</u>	8	<u>59.18.253</u>	AMD	<u>169</u>	3
<u>51.16</u>	ADD	<u>212</u>	4	<u>59.18.290</u>	AMD	<u>315</u>	7
<u>51.28.100</u>	AMD	<u>80</u>	39	<u>59.18.365</u>	AMD	<u>315</u>	4
<u>51.32.110</u>	AMD	<u>213</u>	2	<u>59.18.410</u>	AMD	<u>315</u>	5
<u>51.36</u>	ADD	<u>213</u>	5	<u>62A.9A-525</u>	AMD	<u>60</u>	2
<u>51.36.070</u>	AMD	<u>213</u>	3	<u>63.14.043</u>	AMD	<u>174</u>	2
<u>51.48</u>	ADD	<u>277</u>	7	<u>63.21</u>	ADD	<u>26</u>	15
<u>51.48.010</u>	AMD	<u>277</u>	1	<u>63.21.010</u>	AMD	<u>26</u>	10

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<u>63.21.020</u>	AMD	<u>26</u>	11	<u>69.50.540</u>	AMD	<u>236</u>	4
<u>63.21.030</u>	AMD	<u>26</u>	12	<u>69.50.540</u>	AMD	<u>357</u>	916
<u>63.21.050</u>	AMD	<u>26</u>	13	<u>69.51A.010</u>	REMD	<u>80</u>	44
<u>63.21.060</u>	AMD	<u>26</u>	14	<u>69.52.030</u>	AMD	<u>18</u>	25
<u>63.32</u>	ADD	<u>26</u>	16	<u>69.70</u>	ADD	<u>264</u>	2
<u>63.40</u>	ADD	<u>26</u>	17	<u>70</u>	ADD	<u>102</u>	1-3
<u>64.34</u>	ADD	<u>9</u>	3	<u>70</u>	ADD	<u>121</u>	1-6
<u>64.38</u>	ADD	<u>9</u>	2				8,10
<u>64.50.010</u>	AMD	<u>18</u>	23	<u>70</u>	ADD	<u>138</u>	1-7
<u>64.70.020</u>	AMD	<u>20</u>	1064	<u>70</u>	ADD	<u>340</u>	1-7
<u>64.70.040</u>	AMD	<u>20</u>	1065	<u>70.02.010</u>	AMD	<u>256</u>	401
<u>64.90</u>	ADD	<u>9</u>	4	<u>70.02.010</u>	AMD	<u>302</u>	112
<u>66.24</u>	ADD	<u>186</u>	1	<u>70.02.205</u>	AMD	<u>81</u>	2
<u>66.24</u>	ADD	<u>210</u>	1	<u>70.02.230</u>	REMD	<u>256</u>	402
<u>66.24</u>	ADD	<u>238</u>	3-7	<u>70.05.070</u>	AMD	<u>20</u>	1066
			12	<u>70.14</u>	ADD	<u>346</u>	2,3
<u>66.24.140</u>	AMD	<u>238</u>	1	<u>70.14.060</u>	AMD	<u>346</u>	4
<u>66.24.145</u>	AMD	<u>238</u>	2	<u>70.24</u>	ADD	<u>76</u>	4,5
<u>66.24.240</u>	REMD	<u>230</u>	1	<u>70.24.015</u>	AMD	<u>76</u>	1
<u>66.24.244</u>	AMD	<u>230</u>	2	<u>70.24.017</u>	AMD	<u>76</u>	2
<u>66.24.395</u>	AMD	<u>200</u>	2	<u>70.24.024</u>	AMD	<u>76</u>	3
<u>66.24.425</u>	REMD	<u>274</u>	45	<u>70.24.080</u>	AMD	<u>76</u>	6
<u>66.24.630</u>	AMD	<u>238</u>	9	<u>70.24.095</u>	REP	<u>76</u>	22
<u>66.28.040</u>	AMD	<u>238</u>	8	<u>70.24.100</u>	REP	<u>76</u>	22
<u>66.28.200</u>	AMD	<u>230</u>	3	<u>70.24.107</u>	REP	<u>76</u>	22
<u>66.28.210</u>	AMD	<u>230</u>	4	<u>70.24.110</u>	AMD	<u>76</u>	7
<u>66.28.220</u>	AMD	<u>230</u>	5	<u>70.24.120</u>	AMD	<u>76</u>	8
<u>66.28.310</u>	AMD	<u>200</u>	1	<u>70.24.125</u>	REP	<u>76</u>	22
<u>66.28.310</u>	AMD	<u>238</u>	10	<u>70.24.130</u>	AMD	<u>76</u>	9
<u>68.50.320</u>	AMD	<u>45</u>	2	<u>70.24.140</u>	REP	<u>76</u>	22
<u>68.50.330</u>	AMD	<u>45</u>	3	<u>70.24.200</u>	REP	<u>76</u>	22
<u>68.52.220</u>	AMD	<u>83</u>	6	<u>70.24.210</u>	REP	<u>76</u>	22
<u>69.22.020</u>	AMD	<u>171</u>	1	<u>70.24.220</u>	AMD	<u>76</u>	10
<u>69.41.010</u>	REMD	<u>80</u>	40	<u>70.24.240</u>	REP	<u>76</u>	22
<u>69.41.030</u>	AMD	<u>80</u>	41	<u>70.24.250</u>	REP	<u>76</u>	22
<u>69.41.041</u>	AMD	<u>57</u>	87	<u>70.24.260</u>	REP	<u>76</u>	22
<u>69.41.055</u>	AMD	<u>57</u>	88	<u>70.24.270</u>	REP	<u>76</u>	22
<u>69.41.193</u>	AMD	<u>21</u>	1	<u>70.24.280</u>	REP	<u>76</u>	22
<u>69.45.010</u>	AMD	<u>80</u>	42	<u>70.24.290</u>	AMD	<u>76</u>	11
<u>69.50</u>	ADD	<u>133</u>	4	<u>70.24.300</u>	REP	<u>76</u>	22
<u>69.50</u>	ADD	<u>236</u>	2,5	<u>70.24.310</u>	REP	<u>76</u>	22
<u>69.50.101</u>	REMD	<u>80</u>	43	<u>70.24.320</u>	REP	<u>76</u>	22
<u>69.50.101</u>	REMD	<u>133</u>	2	<u>70.24.325</u>	AMD	<u>76</u>	12
<u>69.50.325</u>	AMD	<u>236</u>	6	<u>70.24.340</u>	AMD	<u>76</u>	13
<u>69.50.331</u>	AMD	<u>154</u>	1	<u>70.24.350</u>	REP	<u>76</u>	22
<u>69.50.342</u>	AMD	<u>133</u>	3	<u>70.24.360</u>	AMD	<u>76</u>	14
<u>69.50.414</u>	AMD	<u>18</u>	24	<u>70.24.370</u>	AMD	<u>76</u>	15

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<u>70.24.380</u>	REP	<u>76</u>	22	<u>70.93.060</u>	RECD	<u>20</u>	2009
<u>70.24.410</u>	REP	<u>76</u>	22	<u>70.93.070</u>	RECD	<u>20</u>	2009
<u>70.38.111</u>	REMD	<u>258</u>	1	<u>70.93.080</u>	RECD	<u>20</u>	2009
<u>70.41.210</u>	AMD	<u>80</u>	45	<u>70.93.090</u>	RECD	<u>20</u>	2009
<u>70.44.050</u>	AMD	<u>83</u>	7	<u>70.93.093</u>	RECD	<u>20</u>	2009
<u>70.47.200</u>	AMD	<u>228</u>	7	<u>70.93.095</u>	AMD	<u>20</u>	1075
<u>70.48.100</u>	AMD	<u>282</u>	1	<u>70.93.095</u>	RECD	<u>20</u>	2009
<u>70.54.400</u>	AMD	<u>80</u>	46	<u>70.93.097</u>	RECD	<u>20</u>	2009
<u>70.58A.010</u>	AMD	<u>312</u>	729	<u>70.93.110</u>	RECD	<u>20</u>	2009
<u>70.75A.005</u>	RECD	<u>20</u>	2007	<u>70.93.180</u>	REMD	<u>20</u>	1076
<u>70.75A.010</u>	RECD	<u>20</u>	2007	<u>70.93.180</u>	RECD	<u>20</u>	2009
<u>70.75A.020</u>	RECD	<u>20</u>	2007	<u>70.93.200</u>	AMD	<u>20</u>	1077
<u>70.75A.020</u>	AMD	<u>23</u>	1	<u>70.93.200</u>	RECD	<u>20</u>	2009
<u>70.75A.030</u>	RECD	<u>20</u>	2007	<u>70.93.210</u>	RECD	<u>20</u>	2009
<u>70.75A.040</u>	AMD	<u>20</u>	1067	<u>70.93.220</u>	AMD	<u>20</u>	1078
<u>70.75A.040</u>	RECD	<u>20</u>	2007	<u>70.93.220</u>	RECD	<u>20</u>	2009
<u>70.75A.050</u>	RECD	<u>20</u>	2007	<u>70.93.230</u>	RECD	<u>20</u>	2009
<u>70.75A.060</u>	AMD	<u>20</u>	1068	<u>70.93.250</u>	AMD	<u>20</u>	1079
<u>70.75A.060</u>	RECD	<u>20</u>	2007	<u>70.93.250</u>	RECD	<u>20</u>	2009
<u>70.76.005</u>	RECD	<u>20</u>	2008	<u>70.93.910</u>	RECD	<u>20</u>	2009
<u>70.76.010</u>	RECD	<u>20</u>	2008	<u>70.94.011</u>	RECD	<u>20</u>	2010
<u>70.76.020</u>	AMD	<u>20</u>	1069	<u>70.94.015</u>	AMD	<u>20</u>	1080
<u>70.76.020</u>	RECD	<u>20</u>	2008	<u>70.94.015</u>	RECD	<u>20</u>	2010
<u>70.76.030</u>	AMD	<u>20</u>	1070	<u>70.94.017</u>	RECD	<u>20</u>	2010
<u>70.76.030</u>	RECD	<u>20</u>	2008	<u>70.94.030</u>	AMD	<u>20</u>	1081
<u>70.76.040</u>	AMD	<u>20</u>	1071	<u>70.94.030</u>	RECD	<u>20</u>	2010
<u>70.76.040</u>	RECD	<u>20</u>	2008	<u>70.94.033</u>	RECD	<u>20</u>	2010
<u>70.76.050</u>	AMD	<u>20</u>	1072	<u>70.94.035</u>	RECD	<u>20</u>	2010
<u>70.76.050</u>	RECD	<u>20</u>	2008	<u>70.94.037</u>	RECD	<u>20</u>	2010
<u>70.76.060</u>	RECD	<u>20</u>	2008	<u>70.94.040</u>	AMD	<u>20</u>	1082
<u>70.76.070</u>	RECD	<u>20</u>	2008	<u>70.94.040</u>	RECD	<u>20</u>	2010
<u>70.76.080</u>	RECD	<u>20</u>	2008	<u>70.94.041</u>	AMD	<u>20</u>	1083
<u>70.76.090</u>	AMD	<u>20</u>	1073	<u>70.94.041</u>	RECD	<u>20</u>	2010
<u>70.76.090</u>	RECD	<u>20</u>	2008	<u>70.94.053</u>	AMD	<u>20</u>	1084
<u>70.76.100</u>	AMD	<u>20</u>	1074	<u>70.94.053</u>	RECD	<u>20</u>	2010
<u>70.76.100</u>	RECD	<u>20</u>	2008	<u>70.94.055</u>	RECD	<u>20</u>	2010
<u>70.76.110</u>	RECD	<u>20</u>	2008	<u>70.94.057</u>	RECD	<u>20</u>	2010
<u>70.82.010</u>	AMD	<u>274</u>	46	<u>70.94.068</u>	RECD	<u>20</u>	2010
<u>70.82.030</u>	AMD	<u>274</u>	47	<u>70.94.069</u>	AMD	<u>20</u>	1085
<u>70.83C.010</u>	AMD	<u>29</u>	16	<u>70.94.069</u>	RECD	<u>20</u>	2010
<u>70.84.010</u>	AMD	<u>274</u>	48	<u>70.94.070</u>	RECD	<u>20</u>	2010
<u>70.84.080</u>	AMD	<u>274</u>	49	<u>70.94.081</u>	RECD	<u>20</u>	2010
<u>70.93.010</u>	RECD	<u>20</u>	2009	<u>70.94.085</u>	RECD	<u>20</u>	2010
<u>70.93.020</u>	RECD	<u>20</u>	2009	<u>70.94.091</u>	RECD	<u>20</u>	2010
<u>70.93.030</u>	RECD	<u>20</u>	2009	<u>70.94.092</u>	RECD	<u>20</u>	2010
<u>70.93.040</u>	RECD	<u>20</u>	2009	<u>70.94.093</u>	RECD	<u>20</u>	2010
<u>70.93.050</u>	RECD	<u>20</u>	2009	<u>70.94.094</u>	RECD	<u>20</u>	2010

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70.94.095	RECD	20	2010	70.94.302	AMD	20	1102
70.94.096	RECD	20	2010	70.94.302	RECD	20	2010
70.94.097	RECD	20	2010	70.94.331	AMD	20	1103
70.94.100	AMD	20	1086	70.94.331	RECD	20	2010
70.94.100	RECD	20	2010	70.94.332	AMD	20	1104
70.94.110	RECD	20	2010	70.94.332	RECD	20	2010
70.94.120	RECD	20	2010	70.94.335	AMD	20	1105
70.94.130	AMD	20	1087	70.94.335	RECD	20	2010
70.94.130	RECD	20	2010	70.94.350	RECD	20	2010
70.94.141	RECD	20	2010	70.94.370	RECD	20	2010
70.94.142	AMD	20	1088	70.94.380	RECD	20	2010
70.94.142	RECD	20	2010	70.94.385	AMD	20	1106
70.94.143	AMD	20	1089	70.94.385	RECD	20	2010
70.94.143	RECD	20	2010	70.94.390	AMD	20	1107
70.94.151	AMD	20	1090	70.94.390	RECD	20	2010
70.94.151	RECD	20	2010	70.94.395	RECD	20	2010
70.94.152	REMD	20	1091	70.94.400	AMD	20	1108
70.94.152	RECD	20	2010	70.94.400	RECD	20	2010
70.94.153	AMD	20	1092	70.94.405	RECD	20	2010
70.94.153	RECD	20	2010	70.94.410	AMD	20	1109
70.94.154	AMD	20	1093	70.94.410	RECD	20	2010
70.94.154	RECD	20	2010	70.94.420	RECD	20	2010
70.94.155	RECD	20	2010	70.94.422	AMD	20	1110
70.94.157	RECD	20	2010	70.94.422	RECD	20	2010
70.94.161	AMD	20	1094	70.94.425	RECD	20	2010
70.94.161	RECD	20	2010	70.94.430	AMD	20	1111
70.94.162	AMD	20	1095	70.94.430	RECD	20	2010
70.94.162	RECD	20	2010	70.94.431	AMD	20	1112
70.94.163	AMD	20	1096	70.94.431	RECD	20	2010
70.94.163	RECD	20	2010	70.94.435	AMD	20	1113
70.94.165	AMD	20	1097	70.94.435	RECD	20	2010
70.94.165	RECD	20	2010	70.94.440	RECD	20	2010
70.94.170	RECD	20	2010	70.94.450	AMD	20	1114
70.94.181	AMD	20	1098	70.94.450	RECD	20	2010
70.94.181	RECD	20	2010	70.94.453	AMD	20	1115
70.94.200	RECD	20	2010	70.94.453	RECD	20	2010
70.94.205	RECD	20	2010	70.94.455	RECD	20	2010
70.94.211	AMD	20	1099	70.94.457	RECD	20	2010
70.94.211	RECD	20	2010	70.94.460	AMD	20	1116
70.94.221	RECD	20	2010	70.94.460	RECD	20	2010
70.94.230	RECD	20	2010	70.94.463	AMD	20	1117
70.94.231	AMD	20	1100	70.94.463	RECD	20	2010
70.94.231	RECD	20	2010	70.94.467	AMD	20	1118
70.94.240	RECD	20	2010	70.94.467	RECD	20	2010
70.94.260	RECD	20	2010	70.94.470	RECD	20	2010
70.94.262	AMD	20	1101	70.94.473	AMD	20	1119
70.94.262	RECD	20	2010	70.94.473	RECD	20	2010

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70.94.475	AMD	20	1120	70.94.6522	AMD	20	1138
70.94.475	RECD	20	2010	70.94.6522	RECD	20	2010
70.94.477	AMD	20	1121	70.94.6524	AMD	20	1139
70.94.477	RECD	20	2010	70.94.6524	RECD	20	2010
70.94.480	AMD	20	1122	70.94.6526	RECD	20	2010
70.94.480	RECD	20	2010	70.94.6528	AMD	20	1140
70.94.483	AMD	20	1123	70.94.6528	RECD	20	2010
70.94.483	RECD	20	2010	70.94.6530	AMD	20	1141
70.94.488	RECD	20	2010	70.94.6530	RECD	20	2010
70.94.510	RECD	20	2010	70.94.6532	AMD	20	1142
70.94.521	RECD	20	2010	70.94.6532	RECD	20	2010
70.94.524	AMD	20	1124	70.94.6534	AMD	20	1143
70.94.524	RECD	20	2010	70.94.6534	RECD	20	2010
70.94.527	AMD	20	1125	70.94.6536	RECD	20	2010
70.94.527	RECD	20	2010	70.94.6538	AMD	20	1144
70.94.528	AMD	20	1126	70.94.6538	RECD	20	2010
70.94.528	RECD	20	2010	70.94.6540	AMD	20	1145
70.94.531	AMD	20	1127	70.94.6540	RECD	20	2010
70.94.531	RECD	20	2010	70.94.6542	AMD	20	1146
70.94.534	AMD	20	1128	70.94.6542	RECD	20	2010
70.94.534	RECD	20	2010	70.94.6544	RECD	20	2010
70.94.537	RECD	20	2010	70.94.6546	AMD	20	1147
70.94.541	AMD	20	1129	70.94.6546	RECD	20	2010
70.94.541	RECD	20	2010	70.94.6548	AMD	20	1148
70.94.544	AMD	20	1130	70.94.6548	RECD	20	2010
70.94.544	RECD	20	2010	70.94.6550	RECD	20	2010
70.94.547	RECD	20	2010	70.94.6552	AMD	20	1149
70.94.551	AMD	20	1131	70.94.6552	RECD	20	2010
70.94.551	RECD	20	2010	70.94.6554	AMD	20	1150
70.94.555	RECD	20	2010	70.94.6554	RECD	20	2010
70.94.600	RECD	20	2010	70.94.6556	AMD	20	1151
70.94.610	RECD	20	2010	70.94.6556	RECD	20	2010
70.94.620	RECD	20	2010	70.94.710	RECD	20	2010
70.94.640	AMD	20	1132	70.94.715	AMD	20	1152
70.94.640	RECD	20	2010	70.94.715	RECD	20	2010
70.94.645	RECD	20	2010	70.94.720	RECD	20	2010
70.94.6511	RECD	20	2010	70.94.725	AMD	20	1153
70.94.6512	AMD	20	1133	70.94.725	RECD	20	2010
70.94.6512	RECD	20	2010	70.94.730	AMD	20	1154
70.94.6514	AMD	20	1134	70.94.730	RECD	20	2010
70.94.6514	RECD	20	2010	70.94.785	AMD	20	1155
70.94.6516	AMD	20	1135	70.94.785	RECD	20	2010
70.94.6516	RECD	20	2010	70.94.800	RECD	20	2010
70.94.6518	AMD	20	1136	70.94.805	AMD	20	1156
70.94.6518	RECD	20	2010	70.94.805	RECD	20	2010
70.94.6520	AMD	20	1137	70.94.820	RECD	20	2010
70.94.6520	RECD	20	2010	70.94.850	AMD	20	1157

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70.94.850	RECD	20	2010	70.95.163	RECD	20	2011
70.94.860	RECD	20	2010	70.95.165	RECD	20	2011
70.94.875	RECD	20	2010	70.95.167	AMD	20	1171
70.94.880	RECD	20	2010	70.95.167	RECD	20	2011
70.94.892	AMD	20	1158	70.95.170	AMD	20	1172
70.94.892	RECD	20	2010	70.95.170	RECD	20	2011
70.94.901	RECD	20	2010	70.95.180	RECD	20	2011
70.94.902	RECD	20	2010	70.95.185	AMD	20	1173
70.94.904	RECD	20	2010	70.95.185	RECD	20	2011
70.94.911	RECD	20	2010	70.95.190	AMD	20	1174
70.94.960	AMD	20	1159	70.95.190	RECD	20	2011
70.94.960	RECD	20	2010	70.95.200	RECD	20	2011
70.94.970	RECD	20	2010	70.95.205	AMD	20	1175
70.94.980	RECD	20	2010	70.95.205	RECD	20	2011
70.94.990	AMD	20	1160	70.95.207	AMD	20	1176
70.94.990	RECD	20	2010	70.95.207	RECD	20	2011
70.94.991	RECD	20	2010	70.95.210	RECD	20	2011
70.94.992	RECD	20	2010	70.95.212	RECD	20	2011
70.95.010	RECD	20	2011	70.95.215	RECD	20	2011
70.95.020	RECD	20	2011	70.95.217	RECD	20	2011
70.95.030	AMD	20	1161	70.95.218	AMD	20	1177
70.95.030	RECD	20	2011	70.95.218	RECD	20	2011
70.95.055	RECD	20	2011	70.95.220	RECD	20	2011
70.95.060	RECD	20	2011	70.95.230	RECD	20	2011
70.95.065	AMD	20	1162	70.95.235	RECD	20	2011
70.95.065	RECD	20	2011	70.95.240	AMD	20	1178
70.95.075	RECD	20	2011	70.95.240	RECD	20	2011
70.95.080	RECD	20	2011	70.95.250	AMD	20	1179
70.95.090	REMD	20	1163	70.95.250	RECD	20	2011
70.95.090	RECD	20	2011	70.95.255	RECD	20	2011
70.95.092	AMD	20	1164	70.95.260	RECD	20	2011
70.95.092	RECD	20	2011	70.95.263	RECD	20	2011
70.95.094	RECD	20	2011	70.95.265	RECD	20	2011
70.95.095	AMD	20	1165	70.95.267	RECD	20	2011
70.95.095	RECD	20	2011	70.95.268	RECD	20	2011
70.95.096	RECD	20	2011	70.95.270	AMD	20	1180
70.95.100	AMD	20	1166	70.95.270	RECD	20	2011
70.95.100	RECD	20	2011	70.95.280	AMD	20	1181
70.95.110	AMD	20	1167	70.95.280	RECD	20	2011
70.95.110	RECD	20	2011	70.95.285	AMD	20	1182
70.95.130	AMD	20	1168	70.95.285	RECD	20	2011
70.95.130	RECD	20	2011	70.95.290	AMD	20	1183
70.95.140	RECD	20	2011	70.95.290	RECD	20	2011
70.95.150	AMD	20	1169	70.95.295	AMD	20	1184
70.95.150	RECD	20	2011	70.95.295	RECD	20	2011
70.95.160	AMD	20	1170	70.95.300	RECD	20	2011
70.95.160	RECD	20	2011	70.95.305	RECD	20	2011

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70.95.306	RECD	20	2011	70.95.670	RECD	20	2011
70.95.310	RECD	20	2011	70.95.700	RECD	20	2011
70.95.315	AMD	20	1185	70.95.710	RECD	20	2011
70.95.315	RECD	20	2011	70.95.715	AMD	20	1202
70.95.320	RECD	20	2011	70.95.715	RECD	20	2011
70.95.330	AMD	20	1186	70.95.720	RECD	20	2011
70.95.330	RECD	20	2011	70.95.725	RECD	20	2011
70.95.400	AMD	20	1187	70.95.805	RECD	20	2011
70.95.400	RECD	20	2011	70.95.807	AMD	20	1203
70.95.410	RECD	20	2011	70.95.807	RECD	20	2011
70.95.420	AMD	20	1188	70.95.810	RECD	20	2011
70.95.420	RECD	20	2011	70.95.815	AMD	20	1204
70.95.430	AMD	20	1189	70.95.815	RECD	20	2011
70.95.430	RECD	20	2011	70.95.900	RECD	20	2011
70.95.440	RECD	20	2011	70.95.903	RECD	20	2011
70.95.500	RECD	20	2011	70.95.904	RECD	20	2011
70.95.510	AMD	20	1190	70.95A.010	RECD	20	2012
70.95.510	RECD	20	2011	70.95A.020	RECD	20	2012
70.95.515	RECD	20	2011	70.95A.030	RECD	20	2012
70.95.521	RECD	20	2011	70.95A.035	RECD	20	2012
70.95.530	AMD	20	1191	70.95A.040	RECD	20	2012
70.95.530	RECD	20	2011	70.95A.045	RECD	20	2012
70.95.532	AMD	20	1192	70.95A.050	RECD	20	2012
70.95.532	RECD	20	2011	70.95A.060	RECD	20	2012
70.95.535	AMD	20	1193	70.95A.070	AMD	20	1205
70.95.535	RECD	20	2011	70.95A.070	RECD	20	2012
70.95.540	RECD	20	2011	70.95A.080	RECD	20	2012
70.95.550	AMD	20	1194	70.95A.090	RECD	20	2012
70.95.550	RECD	20	2011	70.95A.100	AMD	20	1206
70.95.555	AMD	20	1195	70.95A.100	RECD	20	2012
70.95.555	RECD	20	2011	70.95A.910	RECD	20	2012
70.95.560	AMD	20	1196	70.95A.912	RECD	20	2012
70.95.560	RECD	20	2011	70.95A.930	RECD	20	2012
70.95.565	RECD	20	2011	70.95B.010	RECD	20	2013
70.95.570	RECD	20	2011	70.95B.020	RECD	20	2013
70.95.600	RECD	20	2011	70.95B.030	RECD	20	2013
70.95.610	AMD	20	1197	70.95B.040	RECD	20	2013
70.95.610	RECD	20	2011	70.95B.050	RECD	20	2013
70.95.620	RECD	20	2011	70.95B.060	AMD	20	1207
70.95.630	AMD	20	1198	70.95B.060	RECD	20	2013
70.95.630	RECD	20	2011	70.95B.071	RECD	20	2013
70.95.640	RECD	20	2011	70.95B.080	RECD	20	2013
70.95.650	AMD	20	1199	70.95B.090	AMD	20	1208
70.95.650	RECD	20	2011	70.95B.090	RECD	20	2013
70.95.660	AMD	20	1200	70.95B.095	AMD	20	1209
70.95.660	RECD	20	2011	70.95B.095	RECD	20	2013
70.95.670	AMD	20	1201	70.95B.100	RECD	20	2013

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70.95B.110	RECD	20	2013	70.95E.020	AMD	20	1222
70.95B.115	RECD	20	2013	70.95E.020	RECD	20	2016
70.95B.120	AMD	20	1210	70.95E.030	AMD	20	1223
70.95B.120	RECD	20	2013	70.95E.030	RECD	20	2016
70.95B.130	RECD	20	2013	70.95E.040	AMD	20	1224
70.95B.140	RECD	20	2013	70.95E.040	RECD	20	2016
70.95B.151	AMD	20	1211	70.95E.050	AMD	20	1225
70.95B.151	RECD	20	2013	70.95E.050	RECD	20	2016
70.95B.900	RECD	20	2013	70.95E.080	AMD	20	1226
70.95C.010	AMD	20	1212	70.95E.080	RECD	20	2016
70.95C.010	RECD	20	2014	70.95E.090	AMD	20	1227
70.95C.020	AMD	20	1213	70.95E.090	RECD	20	2016
70.95C.020	RECD	20	2014	70.95E.100	RECD	20	2016
70.95C.030	AMD	20	1214	70.95F.010	RECD	20	2017
70.95C.030	RECD	20	2014	70.95F.020	AMD	20	1228
70.95C.040	AMD	20	1215	70.95F.020	RECD	20	2017
70.95C.040	RECD	20	2014	70.95F.030	AMD	20	1229
70.95C.050	RECD	20	2014	70.95F.030	RECD	20	2017
70.95C.060	RECD	20	2014	70.95G.005	RECD	20	2018
70.95C.070	AMD	20	1216	70.95G.010	RECD	20	2018
70.95C.070	RECD	20	2014	70.95G.020	RECD	20	2018
70.95C.080	RECD	20	2014	70.95G.030	AMD	20	1230
70.95C.110	RECD	20	2014	70.95G.030	RECD	20	2018
70.95C.120	RECD	20	2014	70.95G.040	AMD	20	1231
70.95C.200	RECD	20	2014	70.95G.040	RECD	20	2018
70.95C.210	AMD	20	1217	70.95G.050	RECD	20	2018
70.95C.210	RECD	20	2014	70.95G.060	AMD	20	1232
70.95C.220	AMD	20	1218	70.95G.060	RECD	20	2018
70.95C.220	RECD	20	2014	70.95G.070	RECD	20	2018
70.95C.230	AMD	20	1219	70.95I.005	RECD	20	2019
70.95C.230	RECD	20	2014	70.95I.010	AMD	20	1233
70.95C.240	RECD	20	2014	70.95I.010	RECD	20	2019
70.95C.250	RECD	20	2014	70.95I.020	AMD	20	1234
70.95D.010	AMD	20	1220	70.95I.020	RECD	20	2019
70.95D.010	RECD	20	2015	70.95I.030	AMD	20	1235
70.95D.020	RECD	20	2015	70.95I.030	RECD	20	2019
70.95D.030	RECD	20	2015	70.95I.040	AMD	20	1236
70.95D.040	RECD	20	2015	70.95I.040	RECD	20	2019
70.95D.051	RECD	20	2015	70.95I.050	RECD	20	2019
70.95D.060	RECD	20	2015	70.95I.060	AMD	20	1237
70.95D.070	RECD	20	2015	70.95I.060	RECD	20	2019
70.95D.080	RECD	20	2015	70.95I.070	AMD	20	1238
70.95D.090	RECD	20	2015	70.95I.070	RECD	20	2019
70.95D.100	RECD	20	2015	70.95I.080	RECD	20	2019
70.95D.110	RECD	20	2015	70.95I.901	RECD	20	2019
70.95E.010	AMD	20	1221	70.95J.005	RECD	20	2020
70.95E.010	RECD	20	2016	70.95J.007	RECD	20	2020

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70.95J.010	AMD	20	1239	70.95N.020	REMD	20	1247
70.95J.010	RECD	20	2020	70.95N.020	RECD	20	2024
70.95J.020	RECD	20	2020	70.95N.030	RECD	20	2024
70.95J.025	RECD	20	2020	70.95N.040	AMD	20	1248
70.95J.030	RECD	20	2020	70.95N.040	RECD	20	2024
70.95J.040	RECD	20	2020	70.95N.050	RECD	20	2024
70.95J.050	RECD	20	2020	70.95N.060	AMD	20	1249
70.95J.060	RECD	20	2020	70.95N.060	RECD	20	2024
70.95J.070	RECD	20	2020	70.95N.070	AMD	20	1250
70.95J.080	RECD	20	2020	70.95N.070	RECD	20	2024
70.95J.090	AMD	20	1240	70.95N.080	AMD	20	1251
70.95J.090	RECD	20	2020	70.95N.080	RECD	20	2024
70.95K.005	RECD	20	2021	70.95N.090	RECD	20	2024
70.95K.010	AMD	20	1241	70.95N.100	RECD	20	2024
70.95K.010	RECD	20	2021	70.95N.110	RECD	20	2024
70.95K.011	AMD	20	1242	70.95N.120	RECD	20	2024
70.95K.011	RECD	20	2021	70.95N.130	AMD	20	1252
70.95K.020	RECD	20	2021	70.95N.130	RECD	20	2024
70.95K.030	RECD	20	2021	70.95N.140	REMD	20	1253
70.95K.040	RECD	20	2021	70.95N.140	RECD	20	2024
70.95K.900	RECD	20	2021	70.95N.150	RECD	20	2024
70.95K.920	RECD	20	2021	70.95N.160	RECD	20	2024
70.95L.005	RECD	20	2022	70.95N.170	AMD	20	1254
70.95L.010	AMD	20	1243	70.95N.170	RECD	20	2024
70.95L.010	RECD	20	2022	70.95N.180	AMD	20	1255
70.95L.020	RECD	20	2022	70.95N.180	RECD	20	2024
70.95L.030	RECD	20	2022	70.95N.190	AMD	20	1256
70.95L.040	AMD	20	1244	70.95N.190	RECD	20	2024
70.95L.040	RECD	20	2022	70.95N.200	AMD	20	1257
70.95M.010	RECD	20	2023	70.95N.200	RECD	20	2024
70.95M.020	RECD	20	2023	70.95N.210	RECD	20	2024
70.95M.030	RECD	20	2023	70.95N.220	RECD	20	2024
70.95M.040	RECD	20	2023	70.95N.230	AMD	20	1258
70.95M.050	RECD	20	2023	70.95N.230	RECD	20	2024
70.95M.060	RECD	20	2023	70.95N.240	RECD	20	2024
70.95M.070	RECD	20	2023	70.95N.250	RECD	20	2024
70.95M.080	AMD	20	1245	70.95N.260	AMD	20	1259
70.95M.080	RECD	20	2023	70.95N.260	RECD	20	2024
70.95M.090	RECD	20	2023	70.95N.280	AMD	20	1260
70.95M.100	RECD	20	2023	70.95N.280	RECD	20	2024
70.95M.110	AMD	20	1246	70.95N.290	RECD	20	2024
70.95M.110	RECD	20	2023	70.95N.300	AMD	20	1261
70.95M.115	RECD	20	2023	70.95N.300	RECD	20	2024
70.95M.120	RECD	20	2023	70.95N.310	AMD	20	1262
70.95M.130	RECD	20	2023	70.95N.310	RECD	20	2024
70.95M.140	RECD	20	2023	70.95N.320	RECD	20	2024
70.95N.010	RECD	20	2024	70.95N.330	RECD	20	2024

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70.95N.340	RECD	20	2024	70.98.920	RECD	20	2025
70.95N.350	RECD	20	2024	70.99.010	RECD	20	2026
70.95N.900	RECD	20	2024	70.99.020	RECD	20	2026
70.95N.902	RECD	20	2024	70.99.030	RECD	20	2026
70.97.010	REMD	278	1	70.99.040	RECD	20	2026
70.97.030	AMD	278	2	70.99.050	AMD	20	1270
70.97.040	AMD	278	3	70.99.050	RECD	20	2026
70.97.040	AMD	312	730	70.99.060	RECD	20	2026
70.97.050	AMD	278	4	70.99.900	RECD	20	2026
70.97.060	AMD	278	5	70.99.910	RECD	20	2026
70.97.070	AMD	278	6	70.102.010	RECD	20	2027
70.97.080	AMD	278	7	70.102.020	AMD	20	1271
70.97.100	AMD	278	8	70.102.020	RECD	20	2027
70.97.160	AMD	278	9	70.103.010	RECD	20	2028
70.97.200	AMD	278	10	70.103.020	RECD	20	2028
70.97.220	AMD	278	11	70.103.030	AMD	20	1272
70.98.010	RECD	20	2025	70.103.030	RECD	20	2028
70.98.020	AMD	20	1263	70.103.040	AMD	20	1273
70.98.020	RECD	20	2025	70.103.040	RECD	20	2028
70.98.030	RECD	20	2025	70.103.050	AMD	20	1274
70.98.050	RECD	20	2025	70.103.050	RECD	20	2028
70.98.080	RECD	20	2025	70.103.060	AMD	20	1275
70.98.085	AMD	20	1264	70.103.060	RECD	20	2028
70.98.085	RECD	20	2025	70.103.070	AMD	20	1276
70.98.090	RECD	20	2025	70.103.070	RECD	20	2028
70.98.095	AMD	20	1265	70.103.080	RECD	20	2028
70.98.095	RECD	20	2025	70.103.090	RECD	20	2028
70.98.098	AMD	20	1266	70.105.005	AMD	20	1277
70.98.098	RECD	20	2025	70.105.005	RECD	20	2029
70.98.100	RECD	20	2025	70.105.007	RECD	20	2029
70.98.110	RECD	20	2025	70.105.010	AMD	20	1278
70.98.120	RECD	20	2025	70.105.010	RECD	20	2029
70.98.122	AMD	20	1267	70.105.020	AMD	20	1279
70.98.122	RECD	20	2025	70.105.020	RECD	20	2029
70.98.125	RECD	20	2025	70.105.025	RECD	20	2029
70.98.130	RECD	20	2025	70.105.030	RECD	20	2029
70.98.140	RECD	20	2025	70.105.035	AMD	20	1280
70.98.150	RECD	20	2025	70.105.035	RECD	20	2029
70.98.160	RECD	20	2025	70.105.040	RECD	20	2029
70.98.170	RECD	20	2025	70.105.050	AMD	20	1281
70.98.180	RECD	20	2025	70.105.050	RECD	20	2029
70.98.190	RECD	20	2025	70.105.070	RECD	20	2029
70.98.200	RECD	20	2025	70.105.080	RECD	20	2029
70.98.220	AMD	20	1268	70.105.085	RECD	20	2029
70.98.220	RECD	20	2025	70.105.090	AMD	20	1282
70.98.910	AMD	20	1269	70.105.090	RECD	20	2029
70.98.910	RECD	20	2025	70.105.095	RECD	20	2029

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70.105.097	RECD	20	2029	70.105.245	RECD	20	2029
70.105.100	RECD	20	2029	70.105.250	AMD	20	1302
70.105.105	AMD	20	1283	70.105.250	RECD	20	2029
70.105.105	RECD	20	2029	70.105.255	RECD	20	2029
70.105.109	RECD	20	2029	70.105.260	RECD	20	2029
70.105.110	AMD	20	1284	70.105.270	AMD	20	1303
70.105.110	RECD	20	2029	70.105.270	RECD	20	2029
70.105.111	AMD	20	1285	70.105.280	AMD	20	1304
70.105.111	RECD	20	2029	70.105.280	RECD	20	2029
70.105.112	AMD	20	1286	70.105.300	RECD	20	2029
70.105.112	RECD	20	2029	70.105.310	AMD	20	1305
70.105.116	AMD	20	1287	70.105.310	RECD	20	2029
70.105.116	RECD	20	2029	70.105.900	RECD	20	2029
70.105.120	RECD	20	2029	70.105D.010	RECD	20	2030
70.105.130	RECD	20	2029	70.105D.020	REMD	20	1306
70.105.135	AMD	20	1288	70.105D.020	RECD	20	2030
70.105.135	RECD	20	2029	70.105D.030	REMD	18	5
70.105.140	AMD	20	1289	70.105D.030	REMD	20	1307
70.105.140	RECD	20	2029	70.105D.030	RECD	20	2030
70.105.145	AMD	20	1290	70.105D.040	AMD	20	1308
70.105.145	RECD	20	2029	70.105D.040	RECD	20	2030
70.105.150	RECD	20	2029	70.105D.050	AMD	20	1309
70.105.160	AMD	20	1291	70.105D.050	RECD	20	2030
70.105.160	RECD	20	2029	70.105D.055	AMD	20	1310
70.105.165	AMD	20	1292	70.105D.055	RECD	20	2030
70.105.165	RECD	20	2029	70.105D.060	AMD	20	1311
70.105.170	AMD	20	1293	70.105D.060	RECD	20	2030
70.105.170	RECD	20	2029	70.105D.080	AMD	20	1312
70.105.180	AMD	20	1294	70.105D.080	RECD	20	2030
70.105.180	RECD	20	2029	70.105D.090	AMD	20	1313
70.105.200	AMD	20	1295	70.105D.090	RECD	20	2030
70.105.200	RECD	20	2029	70.105D.100	RECD	20	2030
70.105.210	AMD	20	1296	70.105D.110	AMD	20	1314
70.105.210	RECD	20	2029	70.105D.110	RECD	20	2030
70.105.215	RECD	20	2029	70.105D.120	RECD	20	2030
70.105.217	RECD	20	2029	70.105D.130	AMD	20	1315
70.105.220	AMD	20	1297	70.105D.130	RECD	20	2030
70.105.220	RECD	20	2029	70.105D.140	AMD	20	1316
70.105.221	AMD	20	1298	70.105D.140	RECD	20	2030
70.105.221	RECD	20	2029	70.105D.150	RECD	20	2030
70.105.225	AMD	20	1299	70.105D.160	AMD	20	1317
70.105.225	RECD	20	2029	70.105D.160	RECD	20	2030
70.105.230	RECD	20	2029	70.105D.180	AMD	20	1318
70.105.235	AMD	20	1300	70.105D.180	RECD	20	2030
70.105.235	RECD	20	2029	70.105D.190	AMD	20	1319
70.105.240	AMD	20	1301	70.105D.190	RECD	20	2030
70.105.240	RECD	20	2029	70.105D.200	AMD	20	1320

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70.105D.200	RECD	20	2030	70.107.900	RECD	20	2032
70.105D.210	AMD	20	1321	70.107.910	RECD	20	2032
70.105D.210	RECD	20	2030	70.116.010	RECD	20	2033
70.105D.900	RECD	20	2030	70.116.020	RECD	20	2033
70.105D.905	RECD	20	2030	70.116.030	RECD	20	2033
70.105D.910	RECD	20	2030	70.116.040	RECD	20	2033
70.105D.915	RECD	20	2030	70.116.050	AMD	20	1327
70.105D.920	RECD	20	2030	70.116.050	RECD	20	2033
70.105E.010	REP	20	3001	70.116.060	AMD	20	1328
70.105E.020	REP	20	3001	70.116.060	RECD	20	2033
70.105E.030	REP	20	3001	70.116.070	AMD	20	1329
70.105E.040	REP	20	3001	70.116.070	RECD	20	2033
70.105E.050	REP	20	3001	70.116.080	RECD	20	2033
70.105E.060	REP	20	3001	70.116.090	RECD	20	2033
70.105E.080	REP	20	3001	70.116.100	RECD	20	2033
70.105E.100	REP	20	3001	70.116.110	RECD	20	2033
70.105E.900	REP	20	3001	70.116.120	RECD	20	2033
70.105E.901	REP	20	3001	70.116.134	RECD	20	2033
70.106.010	RECD	20	2031	70.116.140	RECD	20	2033
70.106.020	RECD	20	2031	70.118.010	RECD	20	2034
70.106.030	AMD	20	1322	70.118.020	RECD	20	2034
70.106.030	RECD	20	2031	70.118.030	RECD	20	2034
70.106.040	RECD	20	2031	70.118.040	RECD	20	2034
70.106.050	RECD	20	2031	70.118.050	RECD	20	2034
70.106.060	RECD	20	2031	70.118.060	AMD	20	1330
70.106.070	AMD	20	1323	70.118.060	RECD	20	2034
70.106.070	RECD	20	2031	70.118.070	AMD	20	1331
70.106.080	RECD	20	2031	70.118.070	RECD	20	2034
70.106.090	RECD	20	2031	70.118.080	AMD	20	1332
70.106.100	AMD	20	1324	70.118.080	RECD	20	2034
70.106.100	RECD	20	2031	70.118.090	RECD	20	2034
70.106.110	AMD	20	1325	70.118.110	RECD	20	2034
70.106.110	RECD	20	2031	70.118.120	RECD	20	2034
70.106.120	RECD	20	2031	70.118.130	AMD	20	1333
70.106.140	RECD	20	2031	70.118.130	RECD	20	2034
70.106.150	RECD	20	2031	70.118A.010	RECD	20	2035
70.106.905	RECD	20	2031	70.118A.020	AMD	20	1334
70.106.910	RECD	20	2031	70.118A.020	RECD	20	2035
70.107.010	RECD	20	2032	70.118A.030	RECD	20	2035
70.107.020	RECD	20	2032	70.118A.040	AMD	20	1335
70.107.030	RECD	20	2032	70.118A.040	RECD	20	2035
70.107.040	RECD	20	2032	70.118A.050	AMD	20	1336
70.107.050	RECD	20	2032	70.118A.050	RECD	20	2035
70.107.060	RECD	20	2032	70.118A.060	RECD	20	2035
70.107.070	AMD	20	1326	70.118A.070	AMD	20	1337
70.107.070	RECD	20	2032	70.118A.070	RECD	20	2035
70.107.080	RECD	20	2032	70.118A.080	AMD	20	1338

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70.118A.080	RECD	20	2035	70.119A.030	AMD	20	1354
70.118A.090	AMD	20	1339	70.119A.030	RECD	20	2038
70.118A.090	RECD	20	2035	70.119A.040	RECD	20	2038
70.118A.100	RECD	20	2035	70.119A.050	AMD	20	1355
70.118B.005	AMD	20	1340	70.119A.050	RECD	20	2038
70.118B.005	RECD	20	2036	70.119A.060	AMD	20	1356
70.118B.010	RECD	20	2036	70.119A.060	RECD	20	2038
70.118B.020	AMD	20	1341	70.119A.070	RECD	20	2038
70.118B.020	RECD	20	2036	70.119A.080	RECD	20	2038
70.118B.030	AMD	20	1342	70.119A.100	RECD	20	2038
70.118B.030	RECD	20	2036	70.119A.110	AMD	20	1357
70.118B.040	RECD	20	2036	70.119A.110	RECD	20	2038
70.118B.050	RECD	20	2036	70.119A.115	RECD	20	2038
70.118B.060	RECD	20	2036	70.119A.120	AMD	20	1358
70.118B.070	RECD	20	2036	70.119A.120	RECD	20	2038
70.119.010	RECD	20	2037	70.119A.130	RECD	20	2038
70.119.020	RECD	20	2037	70.119A.140	RECD	20	2038
70.119.030	AMD	20	1343	70.119A.150	RECD	20	2038
70.119.030	RECD	20	2037	70.119A.170	RECD	20	2038
70.119.040	RECD	20	2037	70.119A.180	RECD	20	2038
70.119.050	AMD	20	1344	70.119A.190	AMD	20	1359
70.119.050	RECD	20	2037	70.119A.190	RECD	20	2038
70.119.060	AMD	20	1345	70.119A.200	RECD	20	2038
70.119.060	RECD	20	2037	70.119A.210	RECD	20	2038
70.119.070	AMD	20	1346	70.119A.900	RECD	20	2038
70.119.070	RECD	20	2037	70.120.010	AMD	20	1360
70.119.081	RECD	20	2037	70.120.010	RECD	20	2039
70.119.090	AMD	20	1347	70.120.020	RECD	20	2039
70.119.090	RECD	20	2037	70.120.070	AMD	20	1361
70.119.100	AMD	20	1348	70.120.070	RECD	20	2039
70.119.100	RECD	20	2037	70.120.080	AMD	20	1362
70.119.110	RECD	20	2037	70.120.080	RECD	20	2039
70.119.120	AMD	20	1349	70.120.100	RECD	20	2039
70.119.120	RECD	20	2037	70.120.120	AMD	20	1363
70.119.130	AMD	20	1350	70.120.120	RECD	20	2039
70.119.130	RECD	20	2037	70.120.130	AMD	20	1364
70.119.140	RECD	20	2037	70.120.130	RECD	20	2039
70.119.150	AMD	20	1351	70.120.150	RECD	20	2039
70.119.150	RECD	20	2037	70.120.160	RECD	20	2039
70.119.160	RECD	20	2037	70.120.170	RECD	20	2039
70.119.170	AMD	20	1352	70.120.190	AMD	20	1365
70.119.170	RECD	20	2037	70.120.190	RECD	20	2039
70.119.180	RECD	20	2037	70.120.210	RECD	20	2039
70.119.900	RECD	20	2037	70.120.230	RECD	20	2039
70.119A.020	REMD	20	1353	70.120.902	RECD	20	2039
70.119A.020	RECD	20	2038	70.120A.010	AMD	20	1366
70.119A.025	RECD	20	2038	70.120A.010	RECD	20	2040

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<u>70.120A.010</u>	AMD	<u>143</u>	1	<u>70.132.900</u>	RECD	<u>20</u>	2042
<u>70.120A.020</u>	AMD	<u>20</u>	1367	<u>70.138.010</u>	AMD	<u>20</u>	1374
<u>70.120A.020</u>	RECD	<u>20</u>	2040	<u>70.138.010</u>	RECD	<u>20</u>	2043
<u>70.120A.020</u>	REP	<u>143</u>	3	<u>70.138.020</u>	AMD	<u>20</u>	1375
<u>70.120A.030</u>	RECD	<u>20</u>	2040	<u>70.138.020</u>	RECD	<u>20</u>	2043
<u>70.120A.050</u>	RECD	<u>20</u>	2040	<u>70.138.030</u>	AMD	<u>20</u>	1376
<u>70.120A.050</u>	AMD	<u>143</u>	2	<u>70.138.030</u>	RECD	<u>20</u>	2043
<u>70.121.010</u>	RECD	<u>20</u>	2041	<u>70.138.040</u>	RECD	<u>20</u>	2043
<u>70.121.020</u>	AMD	<u>20</u>	1368	<u>70.138.050</u>	RECD	<u>20</u>	2043
<u>70.121.020</u>	RECD	<u>20</u>	2041	<u>70.138.060</u>	RECD	<u>20</u>	2043
<u>70.121.030</u>	RECD	<u>20</u>	2041	<u>70.138.070</u>	RECD	<u>20</u>	2043
<u>70.121.040</u>	RECD	<u>20</u>	2041	<u>70.138.900</u>	RECD	<u>20</u>	2043
<u>70.121.050</u>	AMD	<u>20</u>	1369	<u>70.138.901</u>	RECD	<u>20</u>	2043
<u>70.121.050</u>	RECD	<u>20</u>	2041	<u>70.140.010</u>	RECD	<u>20</u>	2044
<u>70.121.060</u>	AMD	<u>20</u>	1370	<u>70.140.020</u>	RECD	<u>20</u>	2044
<u>70.121.060</u>	RECD	<u>20</u>	2041	<u>70.140.030</u>	RECD	<u>20</u>	2044
<u>70.121.070</u>	AMD	<u>20</u>	1371	<u>70.140.040</u>	RECD	<u>20</u>	2044
<u>70.121.070</u>	RECD	<u>20</u>	2041	<u>70.140.050</u>	RECD	<u>20</u>	2044
<u>70.121.080</u>	AMD	<u>20</u>	1372	<u>70.140.060</u>	RECD	<u>20</u>	2044
<u>70.121.080</u>	RECD	<u>20</u>	2041	<u>70.140.070</u>	RECD	<u>20</u>	2044
<u>70.121.090</u>	RECD	<u>20</u>	2041	<u>70.140.080</u>	RECD	<u>20</u>	2044
<u>70.121.100</u>	RECD	<u>20</u>	2041	<u>70.142.010</u>	RECD	<u>20</u>	2045
<u>70.121.110</u>	AMD	<u>20</u>	1373	<u>70.142.020</u>	RECD	<u>20</u>	2045
<u>70.121.110</u>	RECD	<u>20</u>	2041	<u>70.142.030</u>	RECD	<u>20</u>	2045
<u>70.121.120</u>	RECD	<u>20</u>	2041	<u>70.142.040</u>	RECD	<u>20</u>	2045
<u>70.121.130</u>	RECD	<u>20</u>	2041	<u>70.142.050</u>	AMD	<u>20</u>	1377
<u>70.121.140</u>	RECD	<u>20</u>	2041	<u>70.142.050</u>	RECD	<u>20</u>	2045
<u>70.121.150</u>	RECD	<u>20</u>	2041	<u>70.146.010</u>	RECD	<u>20</u>	2046
<u>70.121.900</u>	RECD	<u>20</u>	2041	<u>70.146.020</u>	RECD	<u>20</u>	2046
<u>70.121.905</u>	RECD	<u>20</u>	2041	<u>70.146.030</u>	AMD	<u>20</u>	1378
<u>70.125.090</u>	AMD	<u>26</u>	4	<u>70.146.030</u>	RECD	<u>20</u>	2046
<u>70.125.090</u>	RECD	<u>26</u>	18	<u>70.146.040</u>	RECD	<u>20</u>	2046
<u>70.125.100</u>	AMD	<u>26</u>	5	<u>70.146.050</u>	RECD	<u>20</u>	2046
<u>70.125.100</u>	RECD	<u>26</u>	18	<u>70.146.060</u>	AMD	<u>20</u>	1379
<u>70.127.040</u>	AMD	<u>258</u>	2	<u>70.146.060</u>	RECD	<u>20</u>	2046
<u>70.128</u>	ADD	<u>220</u>	2	<u>70.146.070</u>	AMD	<u>20</u>	1380
<u>70.128.010</u>	REMD	<u>220</u>	1	<u>70.146.070</u>	RECD	<u>20</u>	2046
<u>70.128.060</u>	AMD	<u>220</u>	3	<u>70.146.075</u>	RECD	<u>20</u>	2046
<u>70.128.120</u>	AMD	<u>80</u>	47	<u>70.146.090</u>	RECD	<u>20</u>	2046
<u>70.129.005</u>	AMD	<u>278</u>	12	<u>70.146.100</u>	AMD	<u>20</u>	1381
<u>70.129.010</u>	AMD	<u>278</u>	13	<u>70.146.100</u>	RECD	<u>20</u>	2046
<u>70.129.160</u>	AMD	<u>278</u>	14	<u>70.146.110</u>	AMD	<u>20</u>	1382
<u>70.132.010</u>	RECD	<u>20</u>	2042	<u>70.146.110</u>	RECD	<u>20</u>	2046
<u>70.132.020</u>	RECD	<u>20</u>	2042	<u>70.146.120</u>	RECD	<u>20</u>	2046
<u>70.132.030</u>	RECD	<u>20</u>	2042	<u>70.148</u>	ADD	<u>156</u>	3
<u>70.132.040</u>	RECD	<u>20</u>	2042	<u>70.148.005</u>	RECD	<u>20</u>	2047
<u>70.132.050</u>	RECD	<u>20</u>	2042	<u>70.148.005</u>	AMD	<u>156</u>	1

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<u>70.148.010</u>	RECD	<u>20</u>	2047	<u>70.150.060</u>	RECD	<u>20</u>	2049
<u>70.148.020</u>	AMD	<u>20</u>	1383	<u>70.150.070</u>	AMD	<u>20</u>	1391
<u>70.148.020</u>	RECD	<u>20</u>	2047	<u>70.150.070</u>	RECD	<u>20</u>	2049
<u>70.148.020</u>	AMD	<u>156</u>	4	<u>70.150.080</u>	RECD	<u>20</u>	2049
<u>70.148.025</u>	AMD	<u>20</u>	1384	<u>70.150.900</u>	RECD	<u>20</u>	2049
<u>70.148.025</u>	RECD	<u>20</u>	2047	<u>70.164.010</u>	RECD	<u>20</u>	2050
<u>70.148.030</u>	RECD	<u>20</u>	2047	<u>70.164.020</u>	AMD	<u>20</u>	1392
<u>70.148.035</u>	RECD	<u>20</u>	2047	<u>70.164.020</u>	RECD	<u>20</u>	2050
<u>70.148.040</u>	RECD	<u>20</u>	2047	<u>70.164.030</u>	AMD	<u>20</u>	1393
<u>70.148.050</u>	RECD	<u>20</u>	2047	<u>70.164.030</u>	RECD	<u>20</u>	2050
<u>70.148.050</u>	AMD	<u>156</u>	2	<u>70.164.040</u>	RECD	<u>20</u>	2050
<u>70.148.060</u>	RECD	<u>20</u>	2047	<u>70.164.050</u>	RECD	<u>20</u>	2050
<u>70.148.070</u>	AMD	<u>20</u>	1385	<u>70.164.060</u>	RECD	<u>20</u>	2050
<u>70.148.070</u>	RECD	<u>20</u>	2047	<u>70.164.070</u>	RECD	<u>20</u>	2050
<u>70.148.080</u>	RECD	<u>20</u>	2047	<u>70.180.030</u>	REMD	<u>80</u>	48
<u>70.148.090</u>	RECD	<u>20</u>	2047	<u>70.185.090</u>	AMD	<u>80</u>	49
<u>70.148.090</u>	AMD	<u>156</u>	5	<u>70.220.010</u>	RECD	<u>20</u>	2051
<u>70.148.110</u>	RECD	<u>20</u>	2047	<u>70.220.020</u>	AMD	<u>20</u>	1394
<u>70.148.900</u>	RECD	<u>20</u>	2047	<u>70.220.020</u>	RECD	<u>20</u>	2051
<u>70.149</u>	ADD	<u>310</u>	3	<u>70.220.030</u>	AMD	<u>20</u>	1395
<u>70.149.010</u>	RECD	<u>20</u>	2048	<u>70.220.030</u>	RECD	<u>20</u>	2051
<u>70.149.010</u>	AMD	<u>310</u>	1	<u>70.220.040</u>	RECD	<u>20</u>	2051
<u>70.149.020</u>	RECD	<u>20</u>	2048	<u>70.220.050</u>	AMD	<u>20</u>	1396
<u>70.149.030</u>	AMD	<u>20</u>	1386	<u>70.220.050</u>	RECD	<u>20</u>	2051
<u>70.149.030</u>	RECD	<u>20</u>	2048	<u>70.225.040</u>	AMD	<u>80</u>	50
<u>70.149.040</u>	AMD	<u>20</u>	1387	<u>70.235</u>	ADD	<u>79</u>	4
<u>70.149.040</u>	RECD	<u>20</u>	2048	<u>70.235</u>	ADD	<u>120</u>	3
<u>70.149.040</u>	AMD	<u>310</u>	2	<u>70.235.005</u>	AMD	<u>20</u>	1397
<u>70.149.050</u>	RECD	<u>20</u>	2048	<u>70.235.005</u>	RECD	<u>20</u>	2052
<u>70.149.050</u>	REP	<u>310</u>	11	<u>70.235.005</u>	AMD	<u>120</u>	2
<u>70.149.060</u>	RECD	<u>20</u>	2048	<u>70.235.010</u>	RECD	<u>20</u>	2052
<u>70.149.070</u>	AMD	<u>20</u>	1388	<u>70.235.010</u>	REMD	<u>79</u>	5
<u>70.149.070</u>	RECD	<u>20</u>	2048	<u>70.235.020</u>	AMD	<u>20</u>	1398
<u>70.149.080</u>	RECD	<u>20</u>	2048	<u>70.235.020</u>	RECD	<u>20</u>	2052
<u>70.149.090</u>	RECD	<u>20</u>	2048	<u>70.235.020</u>	AMD	<u>32</u>	4
<u>70.149.100</u>	RECD	<u>20</u>	2048	<u>70.235.020</u>	AMD	<u>79</u>	2
<u>70.149.120</u>	AMD	<u>20</u>	1389	<u>70.235.030</u>	AMD	<u>20</u>	1399
<u>70.149.120</u>	RECD	<u>20</u>	2048	<u>70.235.030</u>	RECD	<u>20</u>	2052
<u>70.149.800</u>	RECD	<u>20</u>	2048	<u>70.235.040</u>	AMD	<u>20</u>	1400
<u>70.149.801</u>	RECD	<u>20</u>	2048	<u>70.235.040</u>	RECD	<u>20</u>	2052
<u>70.149.900</u>	RECD	<u>20</u>	2048	<u>70.235.050</u>	AMD	<u>20</u>	1401
<u>70.150.010</u>	RECD	<u>20</u>	2049	<u>70.235.050</u>	RECD	<u>20</u>	2052
<u>70.150.020</u>	RECD	<u>20</u>	2049	<u>70.235.050</u>	AMD	<u>79</u>	3
<u>70.150.030</u>	AMD	<u>20</u>	1390	<u>70.235.060</u>	AMD	<u>20</u>	1402
<u>70.150.030</u>	RECD	<u>20</u>	2049	<u>70.235.060</u>	RECD	<u>20</u>	2052
<u>70.150.040</u>	RECD	<u>20</u>	2049	<u>70.235.070</u>	AMD	<u>20</u>	1403
<u>70.150.050</u>	RECD	<u>20</u>	2049	<u>70.235.070</u>	RECD	<u>20</u>	2052

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<u>70.235.080</u>	AMD	<u>20</u>	1404	<u>70.275.160</u>	AMD	<u>20</u>	1418
<u>70.235.080</u>	RECD	<u>20</u>	2052	<u>70.275.160</u>	RECD	<u>20</u>	2056
<u>70.235.900</u>	RECD	<u>20</u>	2052	<u>70.275.170</u>	RECD	<u>20</u>	2056
<u>70.240.010</u>	REMD	<u>20</u>	1405	<u>70.275.900</u>	RECD	<u>20</u>	2056
<u>70.240.010</u>	RECD	<u>20</u>	2053	<u>70.275.901</u>	RECD	<u>20</u>	2056
<u>70.240.020</u>	RECD	<u>20</u>	2053	<u>70.280.010</u>	RECD	<u>20</u>	2057
<u>70.240.025</u>	AMD	<u>20</u>	1406	<u>70.280.020</u>	RECD	<u>20</u>	2057
<u>70.240.025</u>	RECD	<u>20</u>	2053	<u>70.280.030</u>	RECD	<u>20</u>	2057
<u>70.240.030</u>	RECD	<u>20</u>	2053	<u>70.280.040</u>	AMD	<u>20</u>	1419
<u>70.240.035</u>	AMD	<u>20</u>	1407	<u>70.280.040</u>	RECD	<u>20</u>	2057
<u>70.240.035</u>	RECD	<u>20</u>	2053	<u>70.280.050</u>	AMD	<u>20</u>	1420
<u>70.240.040</u>	AMD	<u>20</u>	1408	<u>70.280.050</u>	RECD	<u>20</u>	2057
<u>70.240.040</u>	RECD	<u>20</u>	2053	<u>70.280.060</u>	RECD	<u>20</u>	2057
<u>70.240.050</u>	AMD	<u>20</u>	1409	<u>70.285.010</u>	RECD	<u>20</u>	2058
<u>70.240.050</u>	RECD	<u>20</u>	2053	<u>70.285.020</u>	AMD	<u>20</u>	1421
<u>70.240.060</u>	RECD	<u>20</u>	2053	<u>70.285.020</u>	RECD	<u>20</u>	2058
<u>70.260.010</u>	AMD	<u>20</u>	1410	<u>70.285.030</u>	RECD	<u>20</u>	2058
<u>70.260.010</u>	RECD	<u>20</u>	2054	<u>70.285.040</u>	AMD	<u>20</u>	1422
<u>70.260.020</u>	RECD	<u>20</u>	2054	<u>70.285.040</u>	RECD	<u>20</u>	2058
<u>70.260.030</u>	RECD	<u>20</u>	2054	<u>70.285.050</u>	AMD	<u>20</u>	1423
<u>70.270.010</u>	RECD	<u>20</u>	2055	<u>70.285.050</u>	RECD	<u>20</u>	2058
<u>70.270.020</u>	RECD	<u>20</u>	2055	<u>70.285.060</u>	RECD	<u>20</u>	2058
<u>70.270.030</u>	AMD	<u>20</u>	1411	<u>70.285.070</u>	RECD	<u>20</u>	2058
<u>70.270.030</u>	RECD	<u>20</u>	2055	<u>70.285.080</u>	RECD	<u>20</u>	2058
<u>70.270.040</u>	AMD	<u>20</u>	1412	<u>70.285.090</u>	AMD	<u>20</u>	1424
<u>70.270.040</u>	RECD	<u>20</u>	2055	<u>70.285.090</u>	RECD	<u>20</u>	2058
<u>70.270.050</u>	AMD	<u>20</u>	1413	<u>70.285.100</u>	RECD	<u>20</u>	2058
<u>70.270.050</u>	RECD	<u>20</u>	2055	<u>70.290.060</u>	AMD	<u>291</u>	8
<u>70.270.060</u>	RECD	<u>20</u>	2055	<u>70.295.010</u>	RECD	<u>20</u>	2059
<u>70.275.010</u>	RECD	<u>20</u>	2056	<u>70.295.020</u>	RECD	<u>20</u>	2059
<u>70.275.020</u>	REMD	<u>20</u>	1414	<u>70.300</u>	ADD	<u>67</u>	2,3
<u>70.275.020</u>	RECD	<u>20</u>	2056	<u>70.300.005</u>	RECD	<u>20</u>	2060
<u>70.275.030</u>	AMD	<u>20</u>	1415	<u>70.300.010</u>	RECD	<u>20</u>	2060
<u>70.275.030</u>	RECD	<u>20</u>	2056	<u>70.300.020</u>	RECD	<u>20</u>	2060
<u>70.275.040</u>	AMD	<u>20</u>	1416	<u>70.300.020</u>	AMD	<u>67</u>	1
<u>70.275.040</u>	RECD	<u>20</u>	2056	<u>70.300.030</u>	RECD	<u>20</u>	2060
<u>70.275.050</u>	AMD	<u>20</u>	1417	<u>70.300.040</u>	AMD	<u>20</u>	1425
<u>70.275.050</u>	RECD	<u>20</u>	2056	<u>70.300.040</u>	RECD	<u>20</u>	2060
<u>70.275.060</u>	RECD	<u>20</u>	2056	<u>70.300.050</u>	RECD	<u>20</u>	2060
<u>70.275.070</u>	RECD	<u>20</u>	2056	<u>70.300.060</u>	RECD	<u>20</u>	2060
<u>70.275.080</u>	RECD	<u>20</u>	2056	<u>70.310</u>	ADD	<u>100</u>	1,3
<u>70.275.090</u>	RECD	<u>20</u>	2056	<u>70.310.010</u>	RECD	<u>20</u>	2061
<u>70.275.100</u>	RECD	<u>20</u>	2056	<u>70.310.020</u>	RECD	<u>20</u>	2061
<u>70.275.110</u>	RECD	<u>20</u>	2056	<u>70.310.020</u>	AMD	<u>100</u>	2
<u>70.275.130</u>	RECD	<u>20</u>	2056	<u>70.310.030</u>	AMD	<u>20</u>	1426
<u>70.275.140</u>	RECD	<u>20</u>	2056	<u>70.310.030</u>	RECD	<u>20</u>	2061
<u>70.275.150</u>	RECD	<u>20</u>	2056	<u>70.310.040</u>	AMD	<u>20</u>	1427

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RCW	Action	Bill/Ch.	Sec.	RCW	Action	Bill/Ch.	Sec.
70.310.040	RECD	20	2061	70.340.110	RECD	20	2064
70.310.050	AMD	20	1428	70.340.120	AMD	20	1443
70.310.050	RECD	20	2061	70.340.120	RECD	20	2064
70.315.010	AMD	20	1429	70.340.130	AMD	20	1444
70.315.010	RECD	20	2062	70.340.130	RECD	20	2064
70.315.020	AMD	20	1430	70.340.130	AMD	310	10
70.315.020	RECD	20	2062	70.340.900	AMD	20	1445
70.315.030	RECD	20	2062	70.340.900	RECD	20	2064
70.315.040	RECD	20	2062	70.355.010	RECD	20	2065
70.315.050	AMD	20	1431	70.355.010	AMD	287	1
70.315.050	RECD	20	2062	70.360.010	RECD	20	2066
70.315.060	RECD	20	2062	70.360.020	RECD	20	2066
70.315.900	RECD	20	2062	70.360.030	RECD	20	2066
70.315.901	RECD	20	2062	70.360.040	RECD	20	2066
70.315.902	RECD	20	2062	70.360.050	RECD	20	2066
70.325.010	RECD	20	2063	70.360.060	AMD	20	1446
70.325.020	AMD	20	1432	70.360.060	RECD	20	2066
70.325.020	RECD	20	2063	70.360.070	AMD	20	1447
70.325.030	RECD	20	2063	70.360.070	RECD	20	2066
70.325.040	AMD	20	1433	70.360.080	RECD	20	2066
70.325.040	RECD	20	2063	70.360.090	AMD	20	1448
70.325.050	AMD	20	1434	70.360.090	RECD	20	2066
70.325.050	RECD	20	2063	70.360.100	AMD	20	1449
70.340.010	RECD	20	2064	70.360.100	RECD	20	2066
70.340.010	AMD	310	4	70.360.110	AMD	20	1450
70.340.020	AMD	20	1435	70.360.110	RECD	20	2066
70.340.020	RECD	20	2064	70.360.900	RECD	20	2066
70.340.020	AMD	310	5	70.365.010	REMD	20	1451
70.340.030	AMD	20	1436	70.365.010	RECD	20	2067
70.340.030	RECD	20	2064	70.365.020	AMD	20	1452
70.340.030	AMD	310	6	70.365.020	RECD	20	2067
70.340.040	AMD	20	1437	70.365.030	AMD	20	1453
70.340.040	RECD	20	2064	70.365.030	RECD	20	2067
70.340.050	AMD	20	1438	70.365.040	AMD	20	1454
70.340.050	RECD	20	2064	70.365.040	RECD	20	2067
70.340.050	AMD	310	7	70.365.050	AMD	20	1455
70.340.060	AMD	20	1439	70.365.050	RECD	20	2067
70.340.060	RECD	20	2064	70.365.060	RECD	20	2067
70.340.060	AMD	310	8	70.365.070	AMD	20	1456
70.340.070	RECD	20	2064	70.365.070	RECD	20	2067
70.340.080	AMD	20	1440	70.365.080	AMD	20	1457
70.340.080	RECD	20	2064	70.365.080	RECD	20	2067
70.340.090	AMD	20	1441	70.365.900	RECD	20	2067
70.340.090	RECD	20	2064	70.370.010	RECD	20	2068
70.340.090	AMD	310	9	70.370.020	RECD	20	2068
70.340.100	AMD	20	1442	70.370.030	RECD	20	2068
70.340.100	RECD	20	2064	70.370.040	RECD	20	2068

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RCW	Action	Bill/Ch.	Sec.	RCW	Action	Bill/Ch.	Sec.
<u>70.375.010</u>	RECD	<u>20</u>	2069	<u>71.05.153</u>	AMD	<u>5</u>	4
<u>70.375.020</u>	AMD	<u>20</u>	1458	<u>71.05.153</u>	AMD	<u>5</u>	5
<u>70.375.020</u>	RECD	<u>20</u>	2069	<u>71.05.153</u>	AMD	<u>302</u>	15
<u>70.375.030</u>	RECD	<u>20</u>	2069	<u>71.05.153</u>	AMD	<u>302</u>	16
<u>70.375.040</u>	AMD	<u>20</u>	1459	<u>71.05.153</u>	AMD	<u>302</u>	17
<u>70.375.040</u>	RECD	<u>20</u>	2069	<u>71.05.160</u>	AMD	<u>302</u>	18
<u>70.375.050</u>	AMD	<u>20</u>	1460	<u>71.05.170</u>	AMD	<u>302</u>	19
<u>70.375.050</u>	RECD	<u>20</u>	2069	<u>71.05.180</u>	AMD	<u>302</u>	20
<u>70.375.060</u>	AMD	<u>20</u>	1461	<u>71.05.182</u>	AMD	<u>302</u>	21
<u>70.375.060</u>	RECD	<u>20</u>	2069	<u>71.05.190</u>	AMD	<u>302</u>	22
<u>70.375.070</u>	RECD	<u>20</u>	2069	<u>71.05.195</u>	AMD	<u>302</u>	23
<u>70.375.080</u>	AMD	<u>20</u>	1462	<u>71.05.201</u>	AMD	<u>256</u>	304
<u>70.375.080</u>	RECD	<u>20</u>	2069	<u>71.05.201</u>	AMD	<u>302</u>	24
<u>70.375.090</u>	AMD	<u>20</u>	1463	<u>71.05.210</u>	AMD	<u>302</u>	25
<u>70.375.090</u>	RECD	<u>20</u>	2069	<u>71.05.210</u>	AMD	<u>302</u>	26
<u>70.375.100</u>	RECD	<u>20</u>	2069	<u>71.05.210</u>	AMD	<u>302</u>	27
<u>70.375.110</u>	RECD	<u>20</u>	2069	<u>71.05.212</u>	AMD	<u>256</u>	305
<u>70.375.120</u>	RECD	<u>20</u>	2069	<u>71.05.212</u>	AMD	<u>302</u>	28
<u>70.375.130</u>	RECD	<u>20</u>	2069	<u>71.05.214</u>	AMD	<u>302</u>	29
<u>70.380.010</u>	RECD	<u>20</u>	2070	<u>71.05.215</u>	AMD	<u>302</u>	30
<u>70.380.020</u>	AMD	<u>20</u>	1464	<u>71.05.217</u>	AMD	<u>302</u>	31
<u>70.380.020</u>	RECD	<u>20</u>	2070	<u>71.05.217</u>	AMD	<u>302</u>	32
<u>70.380.030</u>	RECD	<u>20</u>	2070	<u>71.05.230</u>	AMD	<u>302</u>	33
<u>70.380.900</u>	RECD	<u>20</u>	2070	<u>71.05.230</u>	AMD	<u>302</u>	34
<u>71.05</u>	ADD	<u>302</u>	37,101	<u>71.05.235</u>	AMD	<u>302</u>	35
			103	<u>71.05.235</u>	AMD	<u>302</u>	36
<u>71.05.010</u>	AMD	<u>302</u>	1	<u>71.05.240</u>	AMD	<u>302</u>	38
<u>71.05.012</u>	AMD	<u>302</u>	2	<u>71.05.240</u>	AMD	<u>302</u>	39
<u>71.05.020</u>	REMD	<u>5</u>	1	<u>71.05.240</u>	AMD	<u>302</u>	40
<u>71.05.020</u>	REMD	<u>80</u>	51	<u>71.05.280</u>	AMD	<u>302</u>	41
<u>71.05.020</u>	REMD	<u>256</u>	301	<u>71.05.290</u>	AMD	<u>302</u>	42
<u>71.05.020</u>	REMD	<u>302</u>	3	<u>71.05.300</u>	AMD	<u>302</u>	43
<u>71.05.020</u>	REMD	<u>302</u>	4	<u>71.05.310</u>	AMD	<u>302</u>	44
<u>71.05.025</u>	AMD	<u>302</u>	5	<u>71.05.320</u>	AMD	<u>302</u>	45
<u>71.05.026</u>	AMD	<u>302</u>	6	<u>71.05.320</u>	AMD	<u>302</u>	46
<u>71.05.030</u>	AMD	<u>302</u>	7	<u>71.05.360</u>	REP	<u>302</u>	104
<u>71.05.040</u>	AMD	<u>302</u>	8	<u>71.05.360</u>	AMD	<u>312</u>	731
<u>71.05.050</u>	AMD	<u>302</u>	9	<u>71.05.380</u>	AMD	<u>302</u>	47
<u>71.05.100</u>	AMD	<u>302</u>	10	<u>71.05.435</u>	AMD	<u>256</u>	306
<u>71.05.120</u>	AMD	<u>302</u>	11	<u>71.05.445</u>	AMD	<u>302</u>	48
<u>71.05.150</u>	AMD	<u>5</u>	2	<u>71.05.455</u>	AMD	<u>302</u>	49
<u>71.05.150</u>	AMD	<u>5</u>	3	<u>71.05.457</u>	AMD	<u>302</u>	50
<u>71.05.150</u>	AMD	<u>256</u>	302	<u>71.05.525</u>	AMD	<u>302</u>	51
<u>71.05.150</u>	AMD	<u>256</u>	303	<u>71.05.525</u>	RECD	<u>302</u>	105
<u>71.05.150</u>	AMD	<u>302</u>	12	<u>71.05.530</u>	AMD	<u>302</u>	52
<u>71.05.150</u>	AMD	<u>302</u>	13	<u>71.05.585</u>	AMD	<u>302</u>	53
<u>71.05.150</u>	AMD	<u>302</u>	14	<u>71.05.590</u>	AMD	<u>302</u>	54

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RCW	Action	Bill/Ch.	Sec.	RCW	Action	Bill/Ch.	Sec.
71.05.590	AMD	302	55	71.34.610	AMD	185	3
71.05.590	AMD	302	56	71.34.630	AMD	185	4
71.05.720	AMD	302	57	71.34.650	AMD	302	77
71.05.740	AMD	302	58	71.34.700	REMD	302	78
71.05.750	AMD	302	59	71.34.700	REMD	302	79
71.09	ADD	266	6	71.34.710	REMD	302	82
71.12	ADD	115	2,3,8	71.34.710	REMD	302	83
71.12.455	REMD	115	6	71.34.710	REMD	302	84
71.12.480	AMD	115	7	71.34.720	REMD	302	85
71.12.570	AMD	302	114	71.34.720	REMD	302	86
71.24	ADD	256	307	71.34.720	REMD	302	87
71.24	ADD	285	1	71.34.730	AMD	185	5
71.24	ADD	291	2-7	71.34.730	AMD	302	88
71.24	ADD	345	4	71.34.730	AMD	302	89
71.24.025	REMD	80	52	71.34.740	AMD	302	91
71.24.025	REMD	256	201	71.34.740	AMD	302	92
71.24.035	AMD	256	202	71.34.740	AMD	302	93
71.24.061	AMD	291	1	71.34.750	REMD	185	6
71.24.155	AMD	256	203	71.34.750	REMD	185	7
71.24.580	REMD	357	917	71.34.750	REMD	302	94
71.32.020	AMD	80	53	71.34.750	REMD	302	95
71.32.020	AMD	312	732	71.34.780	AMD	302	97
71.34	ADD	185	8	71.34.780	AMD	302	98
71.34	ADD	302	67,69	71A.10.040	AMD	274	51
			73	71A.12.010	AMD	274	52
			80,81	71A.12.020	AMD	274	53
			90,96	71A.16.030	AMD	312	733
			99,100	71A.20.170	AMD	250	1
			102	71A	ADD	41	2,3
			105	71A	ADD	271	1-5
71.34.010	AMD	185	1	72.05.010	AMD	274	54
71.34.010	AMD	302	62	72.05.130	AMD	274	55
71.34.020	REMD	80	54	72.09	ADD	82	4
71.34.020	REMD	185	2	72.09	ADD	319	3,4
71.34.020	REMD	274	50	72.09.015	AMD	319	2
71.34.020	REMD	302	63	72.09.050	REMD	318	5
71.34.020	REMD	302	64	72.09.730	AMD	167	9
71.34.305	AMD	302	65	72.10	ADD	58	1
71.34.310	AMD	302	66	72.10.020	AMD	58	2
71.34.355	AMD	302	68	72.60.235	AMD	274	56
71.34.365	AMD	302	70	72.64.150	AMD	274	57
71.34.370	REP	302	104	72.68	ADD	318	2
71.34.410	AMD	302	71	72.68.001	AMD	318	6
71.34.420	AMD	302	72	72.68.010	AMD	318	4
71.34.500	AMD	302	74	72.68.012	REP	318	7
71.34.600	REMD	302	75	72.68.040	AMD	318	3
71.34.600	REMD	302	76	72.70.010	AMD	274	58

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RCW	Action	Bill/Ch.	Sec.	RCW	Action	Bill/Ch.	Sec.
<u>73.36.050</u>	AMD	<u>312</u>	734	<u>74.26.040</u>	REP	<u>122</u>	1
<u>74</u>	ADD	<u>354</u>	1-10	<u>74.26.050</u>	REP	<u>122</u>	1
			13	<u>74.26.060</u>	REP	<u>122</u>	1
<u>74.04</u>	ADD	<u>136</u>	3	<u>74.34.020</u>	AMD	<u>312</u>	735
<u>74.04.005</u>	AMD	<u>136</u>	1	<u>74.34.067</u>	AMD	<u>312</u>	736
<u>74.04.515</u>	AMD	<u>274</u>	59	<u>74.34.135</u>	AMD	<u>312</u>	737
<u>74.04.805</u>	AMD	<u>322</u>	1	<u>74.34.145</u>	AMD	<u>29</u>	17
<u>74.08.055</u>	AMD	<u>57</u>	89	<u>74.34.163</u>	AMD	<u>312</u>	738
<u>74.08.580</u>	AMD	<u>64</u>	1	<u>74.39.005</u>	AMD	<u>76</u>	21
<u>74.08A</u>	ADD	<u>320</u>	2	<u>74.39A.056</u>	AMD	<u>270</u>	8
<u>74.08A.010</u>	AMD	<u>320</u>	1	<u>74.39A.310</u>	AMD	<u>336</u>	1
<u>74.08A.120</u>	AMD	<u>136</u>	2	<u>74.39A.325</u>	REP	<u>336</u>	2
<u>74.08A.260</u>	REMD	<u>338</u>	1	<u>74.42.010</u>	REMD	<u>80</u>	56
<u>74.09</u>	ADD	<u>4</u>	3	<u>74.42.230</u>	AMD	<u>80</u>	57
<u>74.09</u>	ADD	<u>293</u>	2	<u>74.42.360</u>	AMD	<u>263</u>	3
<u>74.09.010</u>	AMD	<u>80</u>	55	<u>74.42.430</u>	AMD	<u>312</u>	739
<u>74.09.035</u>	AMD	<u>136</u>	4	<u>74.46.561</u>	AMD	<u>357</u>	918
<u>74.09.325</u>	AMD	<u>92</u>	3	<u>77.12.170</u>	AMD	<u>148</u>	5
<u>74.09.390</u>	AMD	<u>242</u>	1	<u>77.12.177</u>	AMD	<u>148</u>	6
<u>74.09.4951</u>	AMD	<u>130</u>	1	<u>77.12.184</u>	AMD	<u>148</u>	7
<u>74.09.522</u>	AMD	<u>260</u>	1	<u>77.12.190</u>	AMD	<u>148</u>	8
<u>74.09A.020</u>	AMD	<u>183</u>	1	<u>77.12.210</u>	AMD	<u>148</u>	9
<u>74.12.290</u>	AMD	<u>274</u>	60	<u>77.12.230</u>	AMD	<u>148</u>	10
<u>74.13</u>	ADD	<u>33</u>	2	<u>77.12.240</u>	AMD	<u>148</u>	11
<u>74.13</u>	ADD	<u>270</u>	1,2	<u>77.12.323</u>	AMD	<u>148</u>	12
<u>74.13.020</u>	AMD	<u>270</u>	4	<u>77.12.380</u>	AMD	<u>148</u>	13
<u>74.13.031</u>	REMD	<u>274</u>	61	<u>77.12.390</u>	AMD	<u>148</u>	14
<u>74.13.1051</u>	AMD	<u>233</u>	3	<u>77.12.670</u>	AMD	<u>148</u>	15
<u>74.13.310</u>	AMD	<u>274</u>	62	<u>77.12.690</u>	AMD	<u>148</u>	16
<u>74.13.350</u>	AMD	<u>41</u>	1	<u>77.15.075</u>	AMD	<u>38</u>	1
<u>74.13.350</u>	RECD	<u>41</u>	6	<u>77.15.100</u>	AMD	<u>38</u>	2
<u>74.13.700</u>	AMD	<u>270</u>	3	<u>77.15.160</u>	REMD	<u>38</u>	3
<u>74.13.802</u>	AMD	<u>33</u>	7	<u>77.15.700</u>	AMD	<u>38</u>	4
<u>74.13A.020</u>	REMD	<u>274</u>	63	<u>77.32.050</u>	AMD	<u>148</u>	17
<u>74.13A.085</u>	AMD	<u>274</u>	64	<u>77.32.430</u>	AMD	<u>148</u>	18
<u>74.14B.070</u>	AMD	<u>331</u>	9	<u>77.32.460</u>	AMD	<u>148</u>	19
<u>74.15.020</u>	AMD	<u>265</u>	1	<u>77.32.470</u>	AMD	<u>148</u>	20
<u>74.15.020</u>	AMD	<u>331</u>	10	<u>77.32.530</u>	AMD	<u>148</u>	21
<u>74.15.100</u>	AMD	<u>73</u>	2	<u>77.32.560</u>	AMD	<u>148</u>	22
<u>74.18.045</u>	AMD	<u>274</u>	65	<u>77.36.070</u>	AMD	<u>148</u>	23
<u>74.20A.055</u>	AMD	<u>227</u>	10	<u>77.36.170</u>	AMD	<u>148</u>	24
<u>74.20A.056</u>	REMD	<u>227</u>	11	<u>77.44.050</u>	AMD	<u>148</u>	25
<u>74.20A.059</u>	AMD	<u>227</u>	12	<u>77.55.011</u>	REMD	<u>10</u>	3
<u>74.26.010</u>	REP	<u>122</u>	1	<u>77.55.021</u>	AMD	<u>10</u>	4
<u>74.26.010</u>	AMD	<u>274</u>	66	<u>77.55.061</u>	AMD	<u>20</u>	1465
<u>74.26.020</u>	REP	<u>122</u>	1	<u>77.55.181</u>	AMD	<u>166</u>	1
<u>74.26.030</u>	REP	<u>122</u>	1	<u>77.70.500</u>	AMD	<u>172</u>	1

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RCW	Action	Bill/Ch.	Sec.	RCW	Action	Bill/Ch.	Sec.
<u>77.125.030</u>	AMD	<u>216</u>	1	<u>82.08.0208</u>	AMD	<u>139</u>	11
<u>79.100.150</u>	AMD	<u>324</u>	3	<u>82.08.02081</u>	REP	<u>139</u>	64
<u>79.100.160</u>	AMD	<u>324</u>	2	<u>82.08.02082</u>	REP	<u>139</u>	64
<u>79.100.170</u>	AMD	<u>324</u>	4	<u>82.08.02087</u>	REP	<u>139</u>	64
<u>79.105.210</u>	AMD	<u>274</u>	67	<u>82.08.02088</u>	REP	<u>139</u>	64
<u>79A.05</u>	ADD	<u>286</u>	1	<u>82.08.025651</u>	AMD	<u>139</u>	12
<u>79A.05.025</u>	AMD	<u>123</u>	1	<u>82.08.02807</u>	AMD	<u>139</u>	13
<u>79A.05.030</u>	AMD	<u>123</u>	2	<u>82.08.0287</u>	AMD	<u>20</u>	1472
<u>79A.55.090</u>	AMD	<u>148</u>	26	<u>82.08.036</u>	AMD	<u>20</u>	1475
<u>79A.80</u>	ADD	<u>75</u>	2	<u>82.08.155</u>	AMD	<u>139</u>	14
<u>79A.80.090</u>	AMD	<u>148</u>	27	<u>82.08.170</u>	AMD	<u>357</u>	919
<u>80.28</u>	ADD	<u>32</u>	2	<u>82.08.195</u>	AMD	<u>139</u>	15
<u>81.40</u>	ADD	<u>170</u>	2-5	<u>82.08.215</u>	AMD	<u>304</u>	1
<u>81.40.010</u>	REP	<u>170</u>	6	<u>82.08.806</u>	AMD	<u>139</u>	16
<u>81.40.035</u>	REP	<u>170</u>	6	<u>82.08.810</u>	AMD	<u>20</u>	1473
<u>81.77.010</u>	AMD	<u>20</u>	1466	<u>82.08.811</u>	AMD	<u>20</u>	1474
<u>81.77.030</u>	AMD	<u>20</u>	1467	<u>82.08.9651</u>	AMD	<u>139</u>	17
<u>81.77.040</u>	AMD	<u>20</u>	1468	<u>82.08.996</u>	AMD	<u>341</u>	1
<u>81.88</u>	ADD	<u>32</u>	3	<u>82.08.998</u>	AMD	<u>20</u>	1476
<u>81.104.140</u>	AMD	<u>1</u>	10	<u>82.12</u>	ADD	<u>350</u>	4
<u>81.104.160</u>	REP	<u>1</u>	11	<u>82.12.0208</u>	AMD	<u>139</u>	18
<u>81.104.160</u>	AMD	<u>1</u>	13	<u>82.12.02081</u>	REP	<u>139</u>	64
<u>81.112</u>	ADD	<u>1</u>	12	<u>82.12.02082</u>	REP	<u>139</u>	64
<u>82</u>	ADD	<u>301</u>	2-5	<u>82.12.02084</u>	REP	<u>139</u>	64
<u>82.04</u>	ADD	<u>138</u>	8	<u>82.12.02085</u>	REP	<u>139</u>	64
<u>82.04</u>	ADD	<u>165</u>	2	<u>82.12.02086</u>	REP	<u>139</u>	64
<u>82.04.050</u>	AMD	<u>80</u>	58	<u>82.12.02087</u>	REP	<u>139</u>	64
<u>82.04.051</u>	AMD	<u>109</u>	2	<u>82.12.02749</u>	AMD	<u>139</u>	19
<u>82.04.192</u>	AMD	<u>139</u>	4	<u>82.12.0282</u>	AMD	<u>20</u>	1477
<u>82.04.260</u>	REMD	<u>165</u>	3	<u>82.12.038</u>	AMD	<u>20</u>	1478
<u>82.04.290</u>	AMD	<u>2</u>	3	<u>82.12.045</u>	AMD	<u>11</u>	1
<u>82.04.299</u>	AMD	<u>2</u>	4	<u>82.12.215</u>	AMD	<u>304</u>	2
<u>82.04.385</u>	REMD	<u>274</u>	68	<u>82.12.225</u>	AMD	<u>159</u>	1
<u>82.04.4266</u>	AMD	<u>139</u>	5	<u>82.12.810</u>	AMD	<u>20</u>	1479
<u>82.04.4268</u>	AMD	<u>139</u>	6	<u>82.12.811</u>	AMD	<u>20</u>	1480
<u>82.04.4269</u>	AMD	<u>139</u>	7	<u>82.12.930</u>	AMD	<u>139</u>	20
<u>82.04.4322</u>	REP	<u>139</u>	64	<u>82.12.9651</u>	AMD	<u>139</u>	22
<u>82.04.4324</u>	REP	<u>139</u>	64	<u>82.12.996</u>	AMD	<u>341</u>	2
<u>82.04.4326</u>	REP	<u>139</u>	64	<u>82.12.998</u>	AMD	<u>20</u>	1481
<u>82.04.4327</u>	AMD	<u>139</u>	8	<u>82.14.049</u>	AMD	<u>139</u>	23
<u>82.04.4328</u>	AMD	<u>139</u>	9	<u>82.14.400</u>	AMD	<u>139</u>	24
<u>82.04.660</u>	AMD	<u>20</u>	1469	<u>82.14.457</u>	AMD	<u>139</u>	25
<u>82.04.755</u>	AMD	<u>20</u>	1470	<u>82.14.530</u>	AMD	<u>222</u>	1
<u>82.04.765</u>	AMD	<u>20</u>	1471	<u>82.16.0497</u>	AMD	<u>139</u>	26
<u>82.08</u>	ADD	<u>350</u>	3	<u>82.16.055</u>	AMD	<u>139</u>	27
<u>82.08.020</u>	AMD	<u>1</u>	7	<u>82.19.040</u>	AMD	<u>20</u>	1482
<u>82.08.0201</u>	AMD	<u>139</u>	10	<u>82.19.040</u>	AMD	<u>357</u>	920

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<u>82.21.030</u>	AMD	<u>20</u>	1483	<u>82.66.901</u>	REP	<u>139</u>	64
<u>82.23A.010</u>	AMD	<u>139</u>	28	<u>82.74.010</u>	AMD	<u>139</u>	44
<u>82.23A.020</u>	AMD	<u>20</u>	1484	<u>82.75.010</u>	AMD	<u>139</u>	45
<u>82.23A.902</u>	AMD	<u>20</u>	1485	<u>82.80.030</u>	AMD	<u>274</u>	69
<u>82.24.010</u>	AMD	<u>139</u>	29	<u>82.80.130</u>	REP	<u>1</u>	6
<u>82.24.551</u>	AMD	<u>139</u>	30	<u>82.80.140</u>	REP	<u>1</u>	6
<u>82.26.010</u>	REMD	<u>139</u>	31	<u>82.82.010</u>	AMD	<u>139</u>	46
<u>82.26.121</u>	AMD	<u>139</u>	32	<u>82.85.030</u>	AMD	<u>139</u>	47
<u>82.26.130</u>	AMD	<u>139</u>	33	<u>82.85.080</u>	AMD	<u>139</u>	48
<u>82.26.190</u>	AMD	<u>139</u>	34	<u>84.14.020</u>	AMD	<u>237</u>	2
<u>82.26.200</u>	AMD	<u>139</u>	35	<u>84.26.050</u>	AMD	<u>91</u>	2
<u>82.27.070</u>	AMD	<u>148</u>	28	<u>84.26.070</u>	AMD	<u>91</u>	1
<u>82.29A.055</u>	AMD	<u>272</u>	2	<u>84.36</u>	ADD	<u>301</u>	1
<u>82.29A.060</u>	AMD	<u>139</u>	36	<u>84.36.010</u>	AMD	<u>272</u>	1
<u>82.29A.120</u>	AMD	<u>139</u>	37	<u>84.36.350</u>	AMD	<u>274</u>	70
<u>82.32.050</u>	AMD	<u>139</u>	60	<u>84.36.383</u>	AMD	<u>209</u>	3
<u>82.32.060</u>	AMD	<u>139</u>	61	<u>84.36.385</u>	AMD	<u>209</u>	2
<u>82.32.062</u>	AMD	<u>139</u>	38	<u>84.36.387</u>	AMD	<u>209</u>	1
<u>82.32.145</u>	AMD	<u>301</u>	6	<u>84.36.400</u>	AMD	<u>204</u>	1
<u>82.32.300</u>	AMD	<u>139</u>	39	<u>84.36.560</u>	AMD	<u>273</u>	1
<u>82.32.385</u>	AMD	<u>219</u>	703	<u>84.36.815</u>	AMD	<u>273</u>	2
<u>82.32.755</u>	REP	<u>139</u>	64	<u>84.36.840</u>	AMD	<u>139</u>	49
<u>82.32.760</u>	REP	<u>139</u>	64	<u>84.37.040</u>	AMD	<u>139</u>	50
<u>82.32.780</u>	AMD	<u>139</u>	40	<u>84.38.040</u>	AMD	<u>139</u>	51
<u>82.32.805</u>	AMD	<u>139</u>	57	<u>84.38.050</u>	AMD	<u>139</u>	52
<u>82.32.808</u>	AMD	<u>139</u>	58	<u>84.38.110</u>	AMD	<u>139</u>	53
<u>82.33.060</u>	AMD	<u>218</u>	5	<u>84.39.020</u>	AMD	<u>139</u>	54
<u>82.34.030</u>	AMD	<u>20</u>	1486	<u>84.39.030</u>	AMD	<u>139</u>	55
<u>82.34.100</u>	AMD	<u>20</u>	1487	<u>84.40.320</u>	AMD	<u>134</u>	1
<u>82.44</u>	ADD	<u>1</u>	8	<u>84.52.043</u>	AMD	<u>253</u>	3
<u>82.44.015</u>	AMD	<u>20</u>	1488	<u>84.52.105</u>	AMD	<u>253</u>	2
<u>82.44.035</u>	REP	<u>1</u>	11	<u>84.55</u>	ADD	<u>179</u>	6
<u>82.44.065</u>	AMD	<u>1</u>	9	<u>84.56.070</u>	AMD	<u>175</u>	1
<u>82.44.135</u>	AMD	<u>219</u>	706	<u>84.56.150</u>	AMD	<u>139</u>	56
<u>82.48.100</u>	REMD	<u>304</u>	4	<u>85.05.410</u>	AMD	<u>83</u>	8
<u>82.48.100</u>	AMD	<u>304</u>	5	<u>85.06.380</u>	AMD	<u>83</u>	9
<u>82.58.005</u>	DECD	<u>139</u>	65	<u>85.08.320</u>	AMD	<u>83</u>	10
<u>82.58.901</u>	DECD	<u>139</u>	65	<u>85.24.080</u>	AMD	<u>83</u>	11
<u>82.58.902</u>	DECD	<u>139</u>	65	<u>85.38.075</u>	AMD	<u>83</u>	12
<u>82.60.025</u>	AMD	<u>139</u>	41	<u>86.09.283</u>	AMD	<u>83</u>	13
<u>82.60.063</u>	AMD	<u>139</u>	42	<u>86.15.055</u>	AMD	<u>83</u>	14
<u>82.63.010</u>	AMD	<u>139</u>	43	<u>87.03.460</u>	AMD	<u>83</u>	15
<u>82.66.010</u>	REP	<u>139</u>	64	<u>88.02.380</u>	AMD	<u>324</u>	5
<u>82.66.020</u>	REP	<u>139</u>	64	<u>88.40.011</u>	REMD	<u>20</u>	1489
<u>82.66.040</u>	REP	<u>139</u>	64	<u>88.46.010</u>	AMD	<u>20</u>	1490
<u>82.66.050</u>	REP	<u>139</u>	64	<u>89</u>	ADD	<u>246</u>	1,2
<u>82.66.060</u>	REP	<u>139</u>	64	<u>89.08</u>	ADD	<u>351</u>	2-7

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<u>90.03.383</u>	AMD	<u>20</u>	1491				
<u>90.03.386</u>	AMD	<u>20</u>	1492				
<u>90.03.570</u>	AMD	<u>20</u>	1493				
<u>90.03.590</u>	AMD	<u>20</u>	1494				
<u>90.46.005</u>	AMD	<u>20</u>	1495				
<u>90.46.010</u>	AMD	<u>20</u>	1496				
<u>90.46.120</u>	AMD	<u>20</u>	1497				
<u>90.48</u>	ADD	<u>10</u>	2				
<u>90.48.039</u>	AMD	<u>20</u>	1498				
<u>90.48.110</u>	AMD	<u>20</u>	1499				
<u>90.48.162</u>	AMD	<u>20</u>	1500				
<u>90.48.285</u>	AMD	<u>20</u>	1501				
<u>90.48.530</u>	AMD	<u>20</u>	1502				
<u>90.48.531</u>	AMD	<u>20</u>	1503				
<u>90.52.030</u>	AMD	<u>20</u>	1504				
<u>90.56.010</u>	REMD	<u>20</u>	1505				
<u>90.56.510</u>	AMD	<u>357</u>	921				
<u>90.58.080</u>	AMD	<u>113</u>	2				
<u>90.58.355</u>	AMD	<u>20</u>	1506				
<u>90.71.270</u>	AMD	<u>20</u>	1507				
<u>90.71.340</u>	AMD	<u>20</u>	1508				
<u>90.71.370</u>	AMD	<u>20</u>	1509				
<u>90.76.005</u>	RECD	<u>20</u>	2071				
<u>90.76.010</u>	RECD	<u>20</u>	2071				
<u>90.76.020</u>	RECD	<u>20</u>	2071				
<u>90.76.040</u>	AMD	<u>20</u>	1510				
<u>90.76.040</u>	RECD	<u>20</u>	2071				
<u>90.76.050</u>	AMD	<u>20</u>	1511				
<u>90.76.050</u>	RECD	<u>20</u>	2071				
<u>90.76.060</u>	RECD	<u>20</u>	2071				
<u>90.76.070</u>	AMD	<u>20</u>	1512				
<u>90.76.070</u>	RECD	<u>20</u>	2071				
<u>90.76.080</u>	RECD	<u>20</u>	2071				
<u>90.76.090</u>	AMD	<u>20</u>	1513				
<u>90.76.090</u>	RECD	<u>20</u>	2071				
<u>90.76.100</u>	AMD	<u>20</u>	1514				
<u>90.76.100</u>	RECD	<u>20</u>	2071				
<u>90.76.110</u>	AMD	<u>20</u>	1515				
<u>90.76.110</u>	RECD	<u>20</u>	2071				
<u>90.76.900</u>	RECD	<u>20</u>	2071				
<u>90.76.901</u>	RECD	<u>20</u>	2071				
<u>90.76.902</u>	AMD	<u>20</u>	1516				
<u>90.76.902</u>	RECD	<u>20</u>	2071				